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NOTES:
1.HSF Property:Comply iSupplier system HSF property attribute up-to-date value.

ELETRO-X TECHNICAL

LLARGA
MV BUILD

2019.05.13

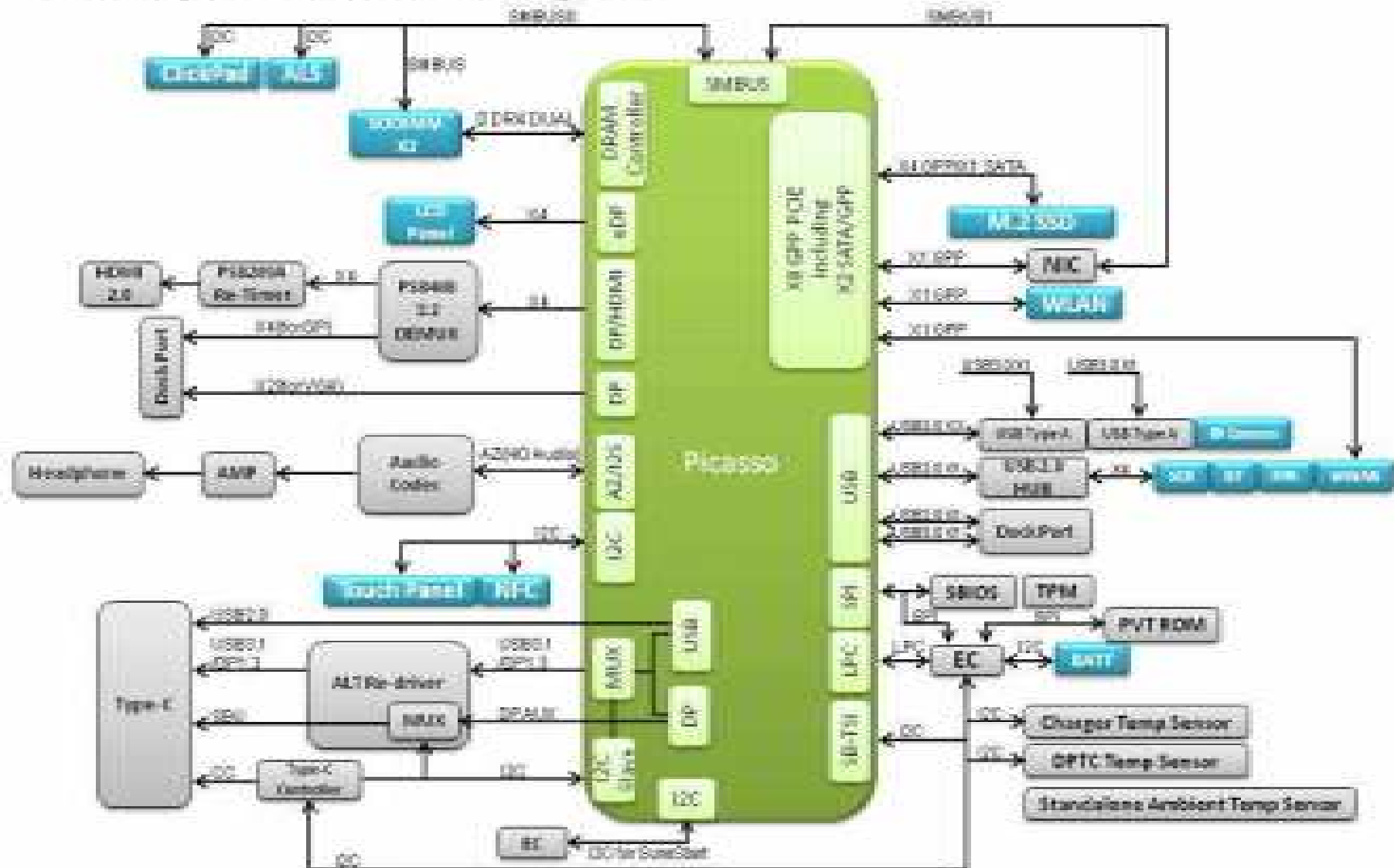
ELETRO-X TECHNICAL

21-OCT-2002		
DATE	CHANGE NO.	REV

DESIGN/DRAWER	XXX	DATE	21-OCT-2002
CHECK	XXXXXXXX		
APPROVAL	XXXXXXXX		
FILE NAME	LLARGA.MV.BUILD.A32		
PCB PIN	XXXXXXXXXXXXXX	PCB VER	XXX

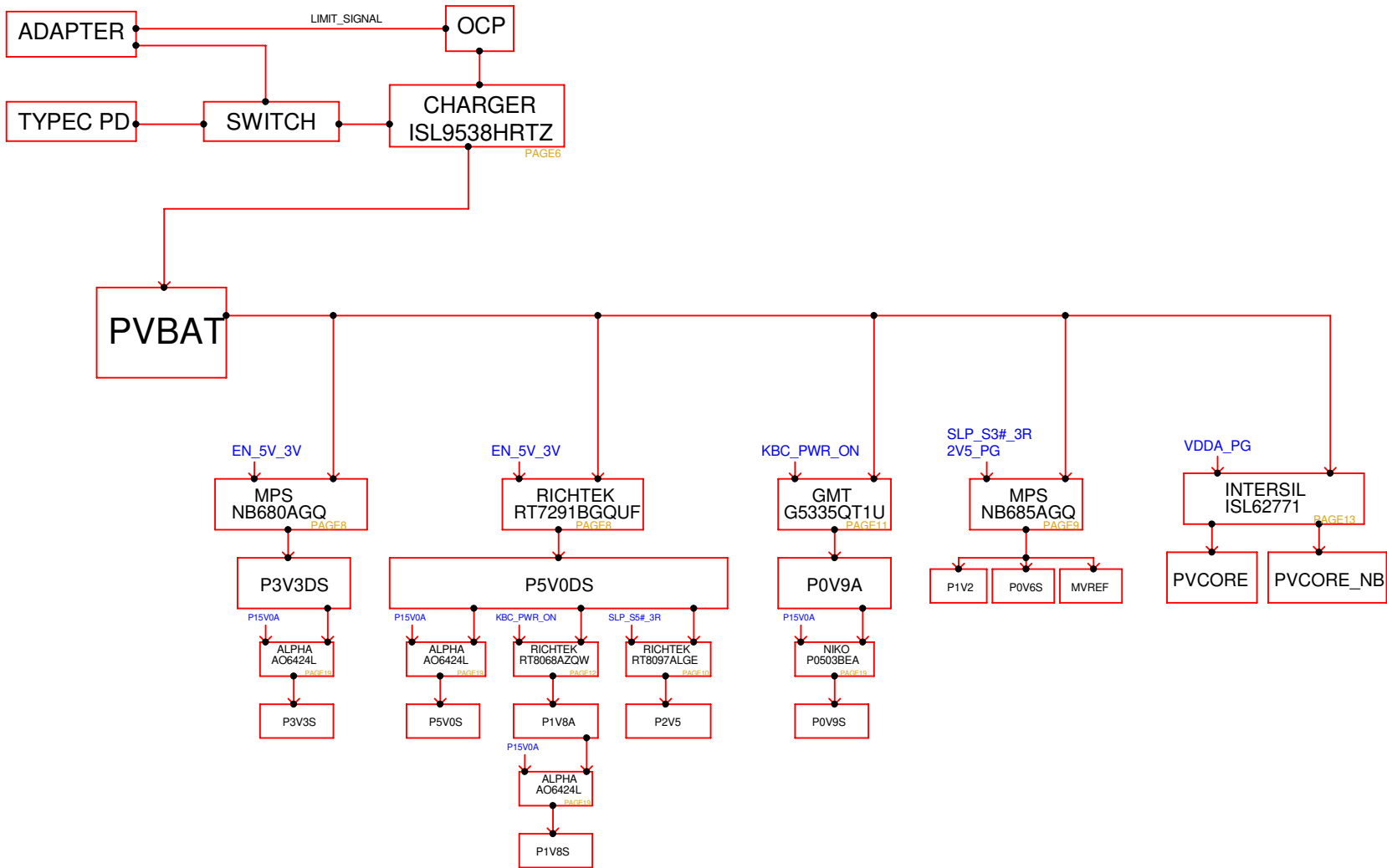
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		MAIN BOARD	
SIZE	CODE	DOC NUMBER	REV
A3	S	1310XXXX-010	X01
SHEET		of 03	

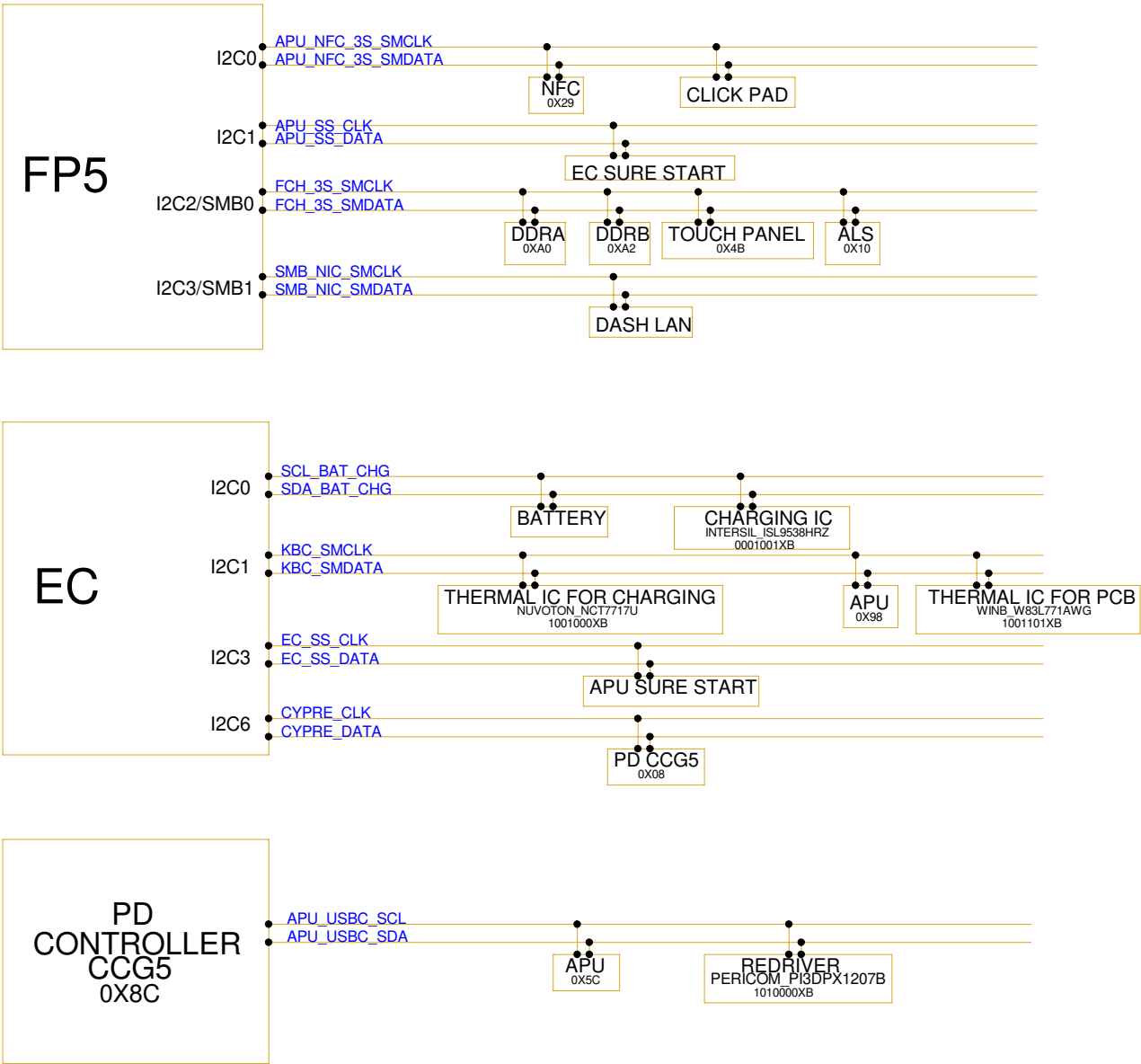
700 System Block Diagram

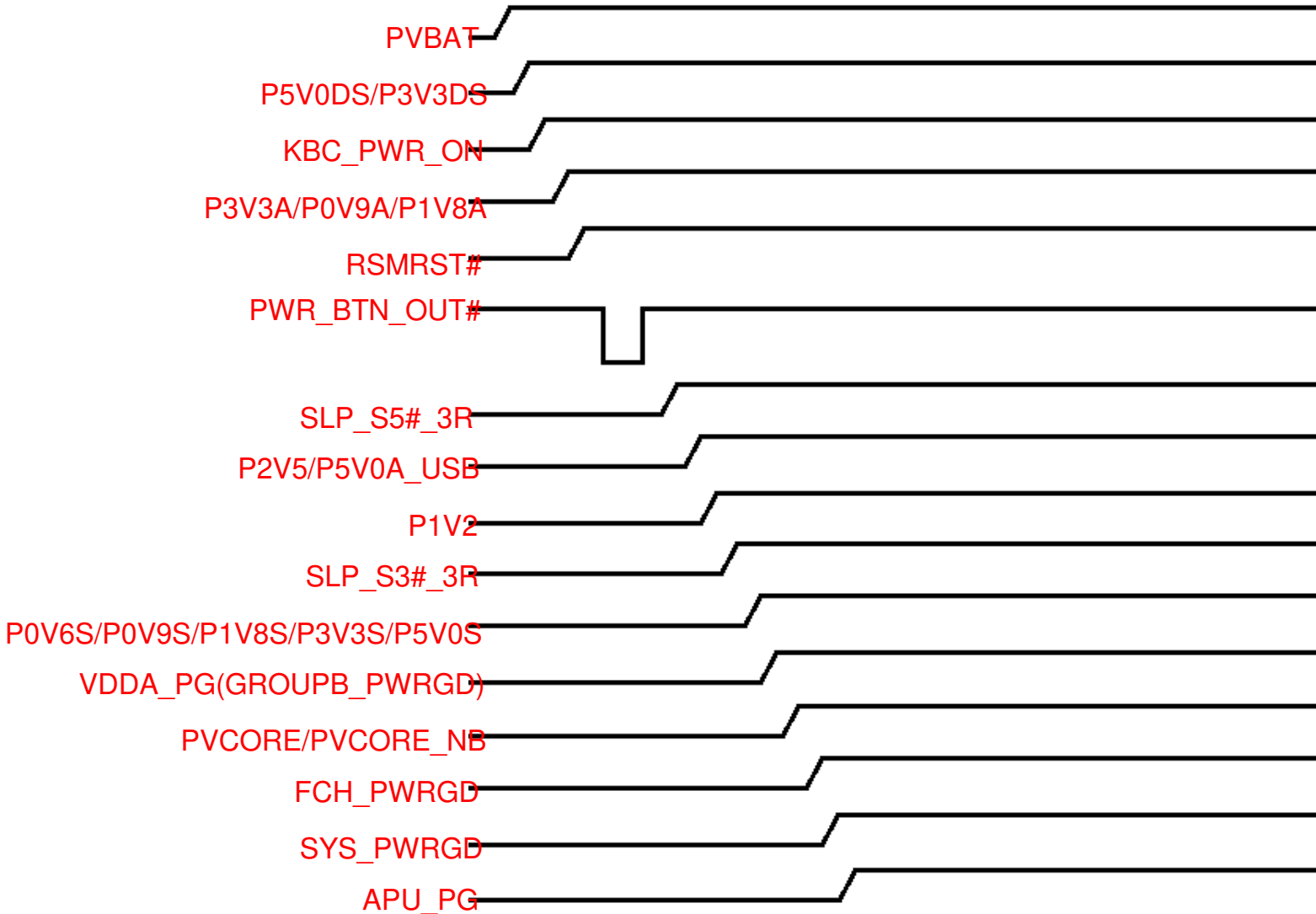


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SIGNAL	VOTAGE	G3	S5	S3	S0	REMARK
P1V5A_RTC	1.5V	O	O	O	O	COIN BATTERY
PVBAT	19V	X	O	O	O	DC-IN ALWAYS
P3V3DS	3.3V	X	O	O	O	DC-IN ALWAYS
P5V0DS	5V	X	O	O	O	DC-IN ALWAYS
P3V3A	3.3V	X	O	O	O	
P1V8A	1.8V	X	O	O	O	
P0V9A	0.9V	X	O	O	O	
P2V5	2.5V	X	X	O	O	
P5V0A_USB	5V	X	X	O	O	
P1V2	1.2V	X	X	O	O	
P5V0S	5V	X	X	X	O	
P3V3S	3.3V	X	X	X	O	
P1V8S	1.8V	X	X	X	O	
P0V9S	0.9V	X	X	X	O	
P0V6S	0.6V	X	X	X	O	
PVCORE_NB	SVC/SVD	X	X	X	O	
PVCORE	SVC/SVD	X	X	X	O	

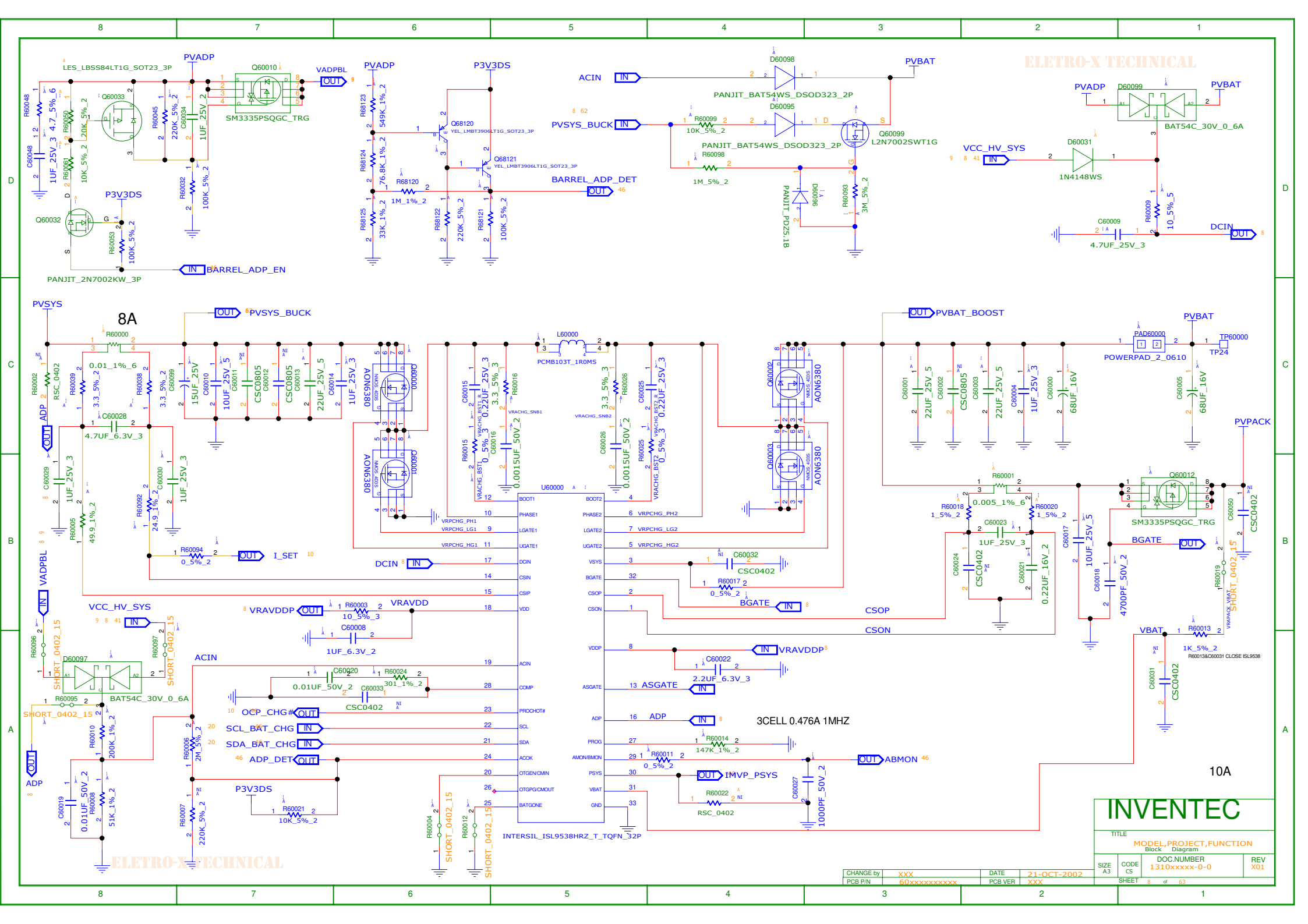
INTERFACE	PORT ASSIGNMENT
USBC/DP#0	USB TYPE-C
USBC/DP#1	NA
USB3_0#1	WALK UP PORT0
USB3_0#2	WALK UP PORT1
USB3_0#3	DOCK PORT
GPP0	WWAN
GPP1	NIC
GPP2	WLAN
GPP3	NA
GPP4	M.2 SSD
GPP5	M.2 SSD
GPP6	M.2 SSD
GPP7	M.2 SSD
DDI0	EDP
DDI1	HDMI / DOCK PORT
DDI2	DOCK PORT VGA
SATA0	NA
SATA1	M.2 SSD

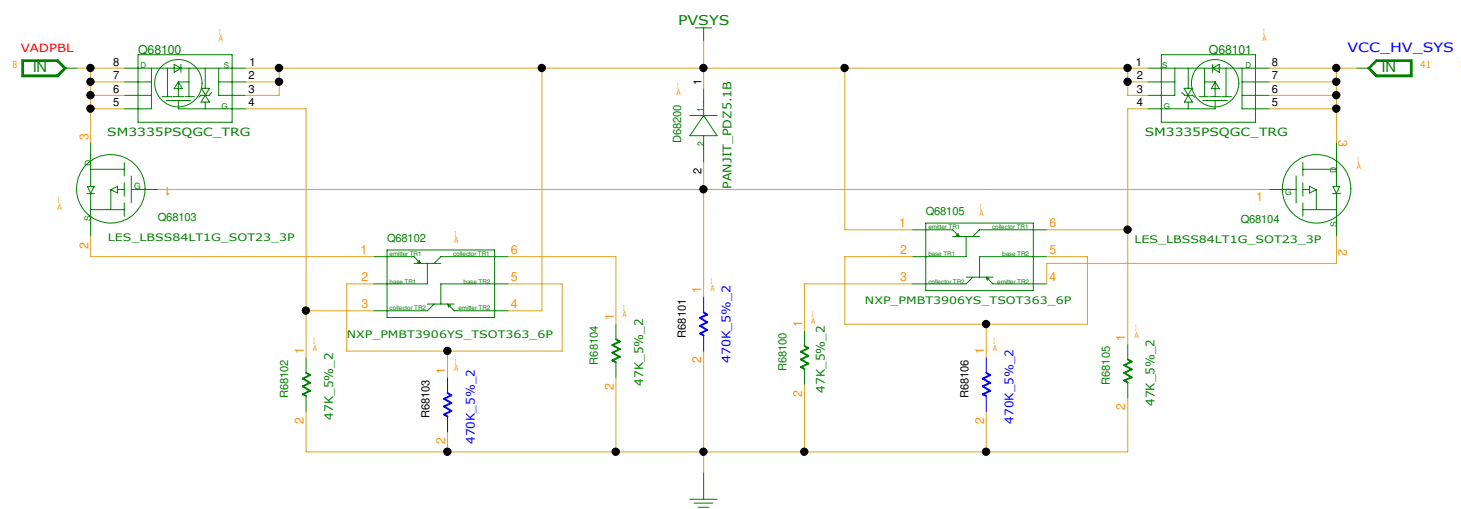
USB 2.0	PORT ASSIGNMENT
USB2_0#0	USB TYPE-C
USB2_0#1	WALK UP PORT0
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USB2_1#0	USB2.0 HUB
USB2_1#1	CAMERA

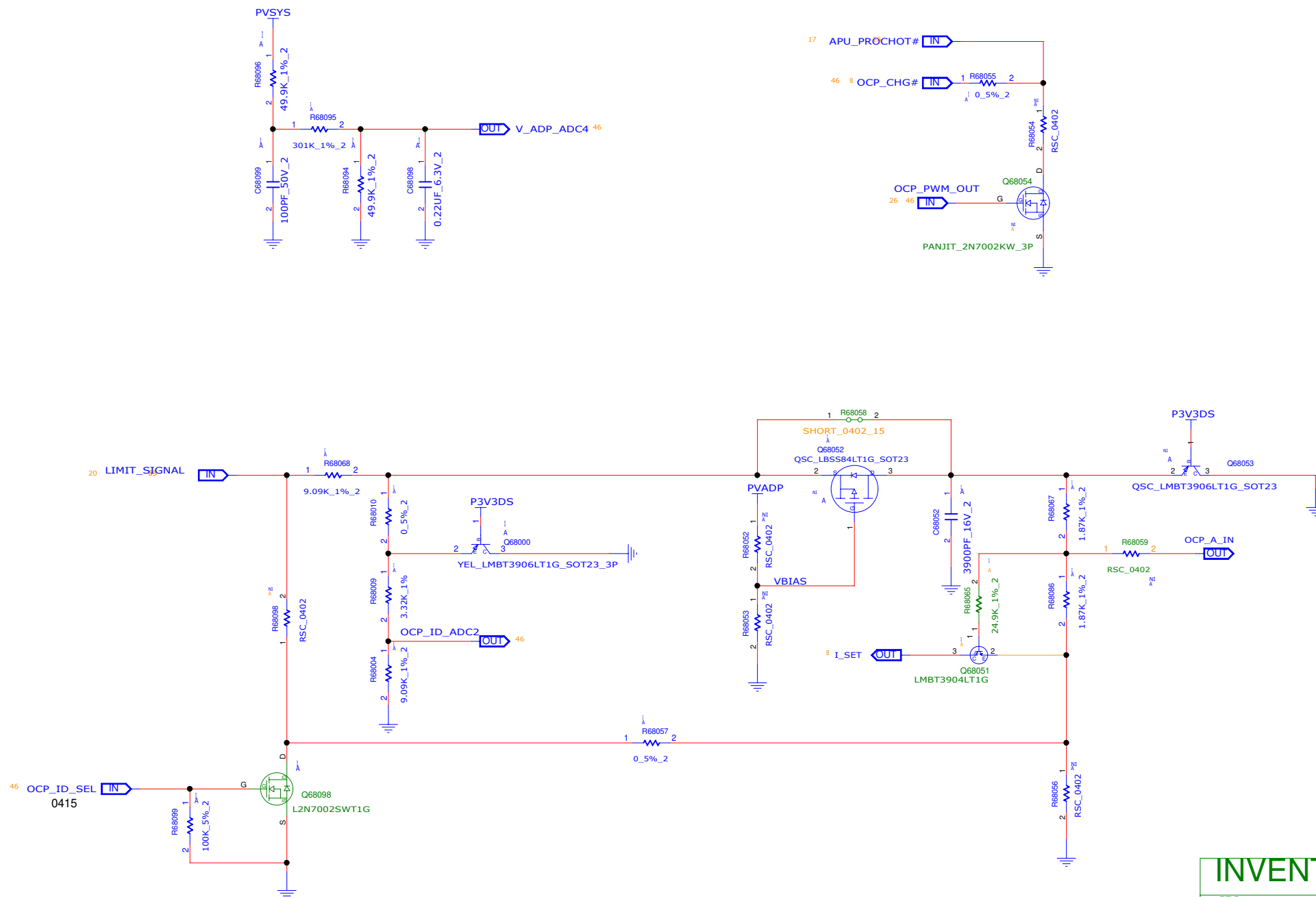
ELETRO-X TECHNICAL

INVENTEC

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BLOCK			Diagram		
DOC NUMBER			1310xxxxx-0-0		
REV			X01		

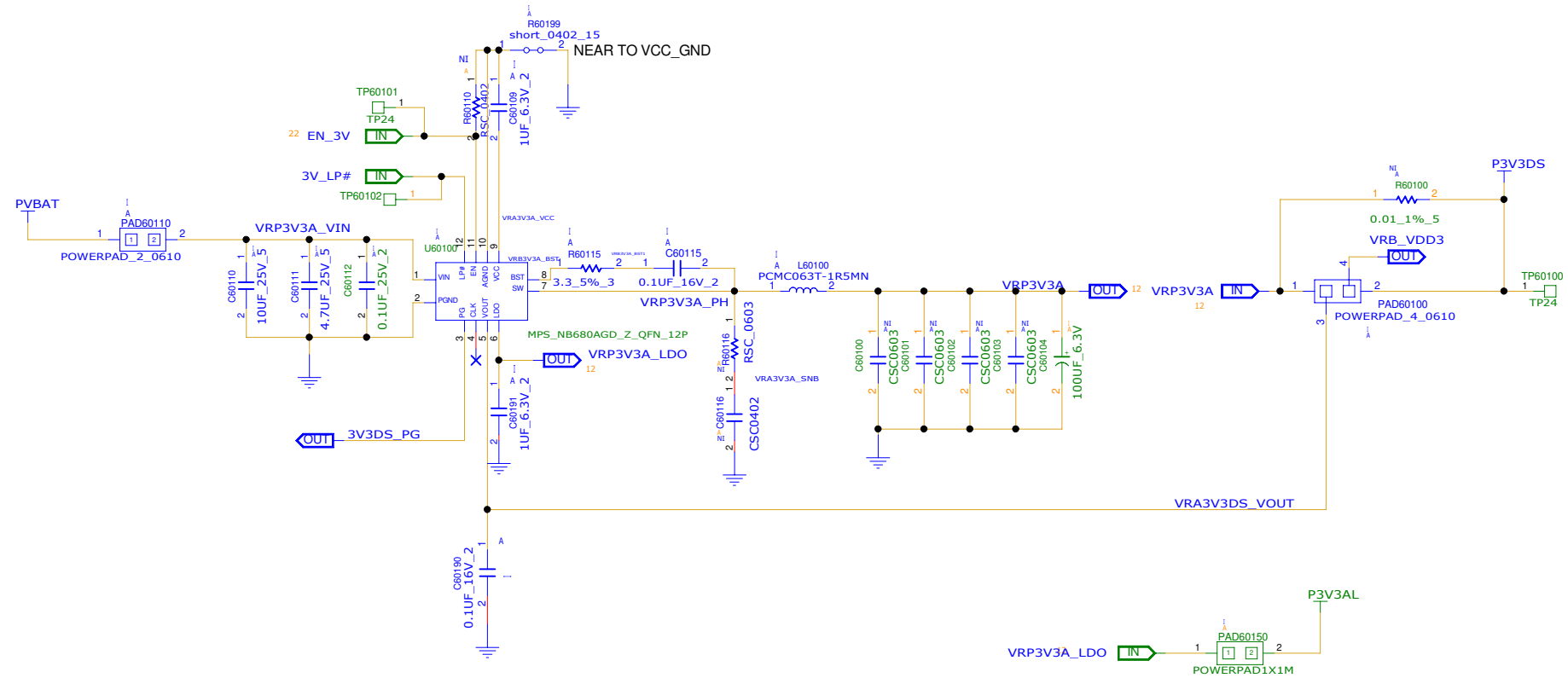


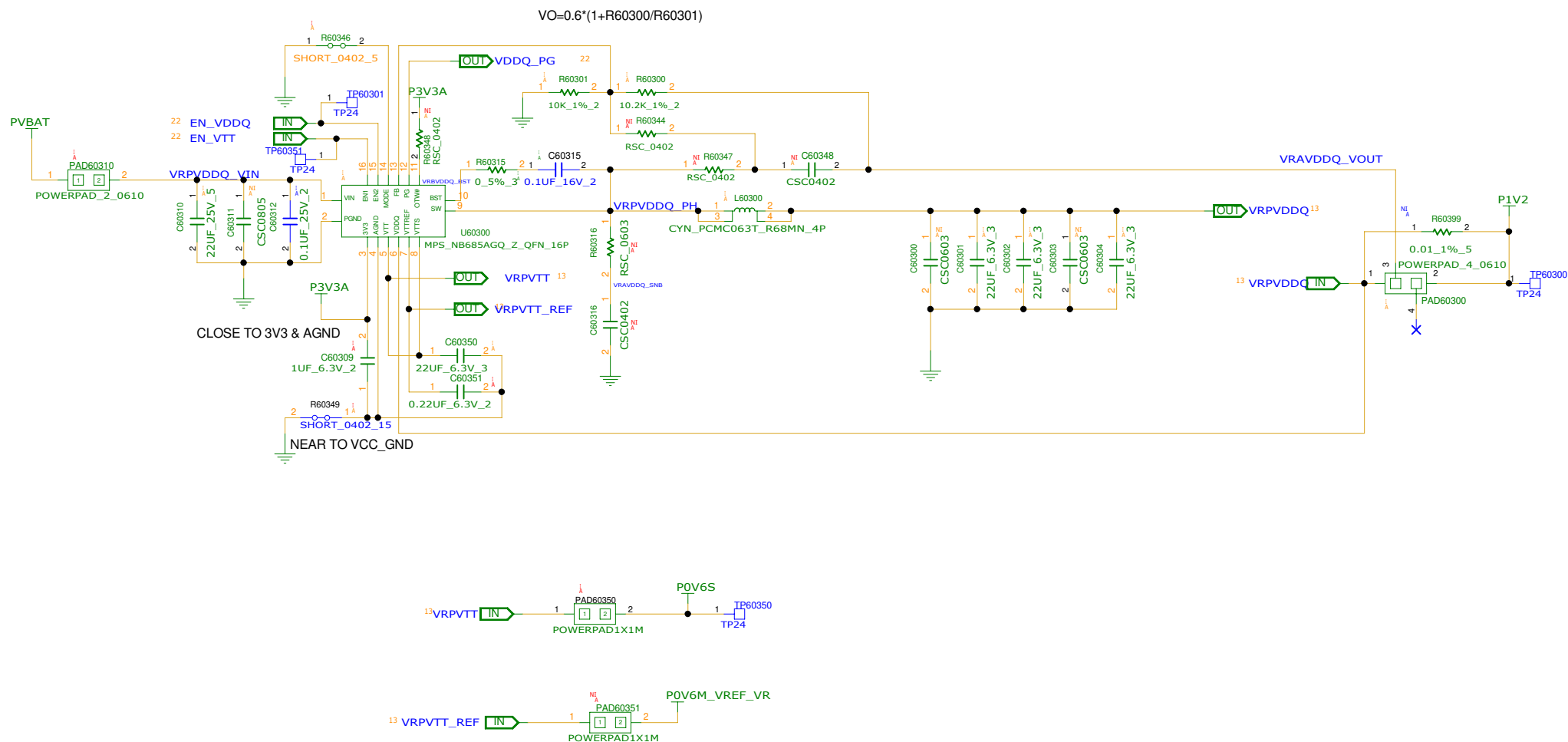


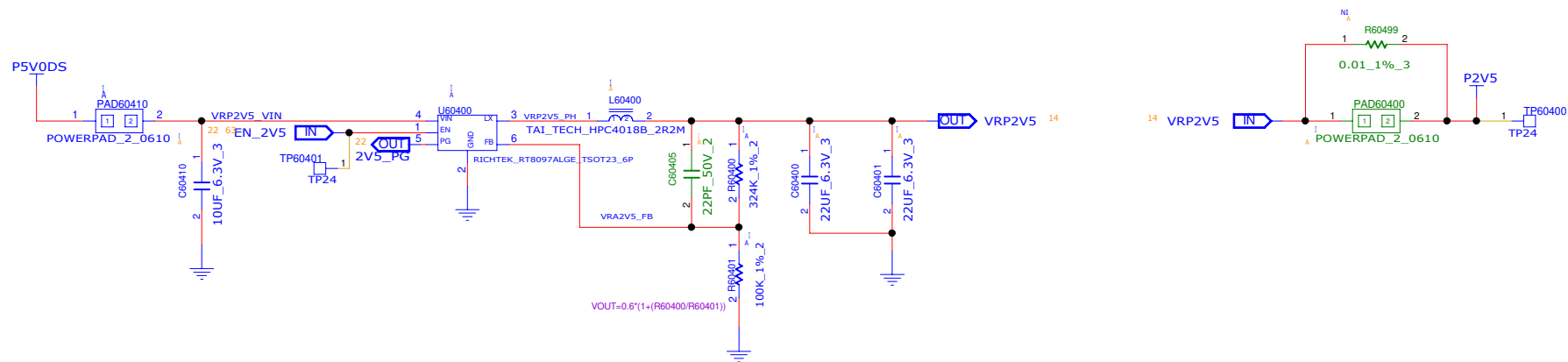


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PCB VER21-OCT-2002
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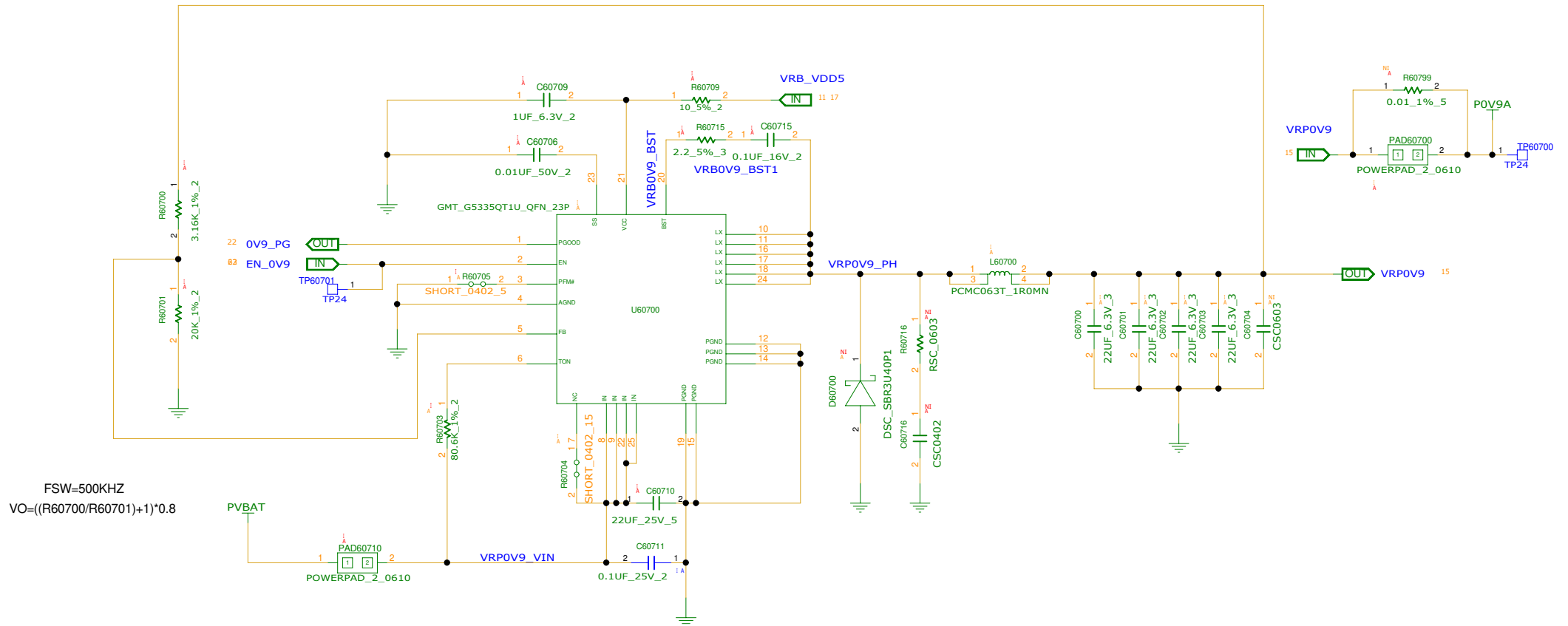


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CHANGE by XXX
PCB P/N 60xxxxxxxxxxx
DATE 21-OCT-2002
PCB VER XXX

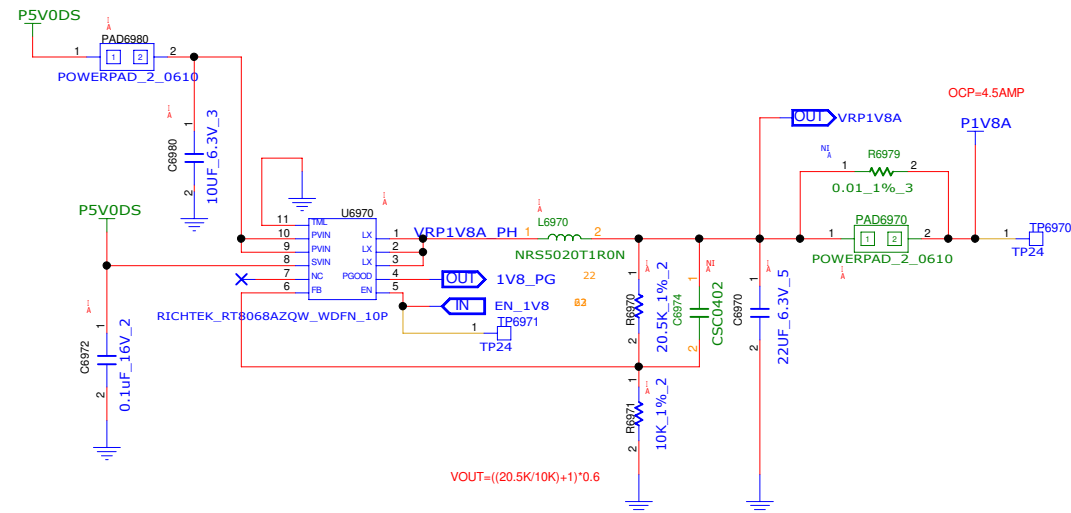


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	Block Diagram

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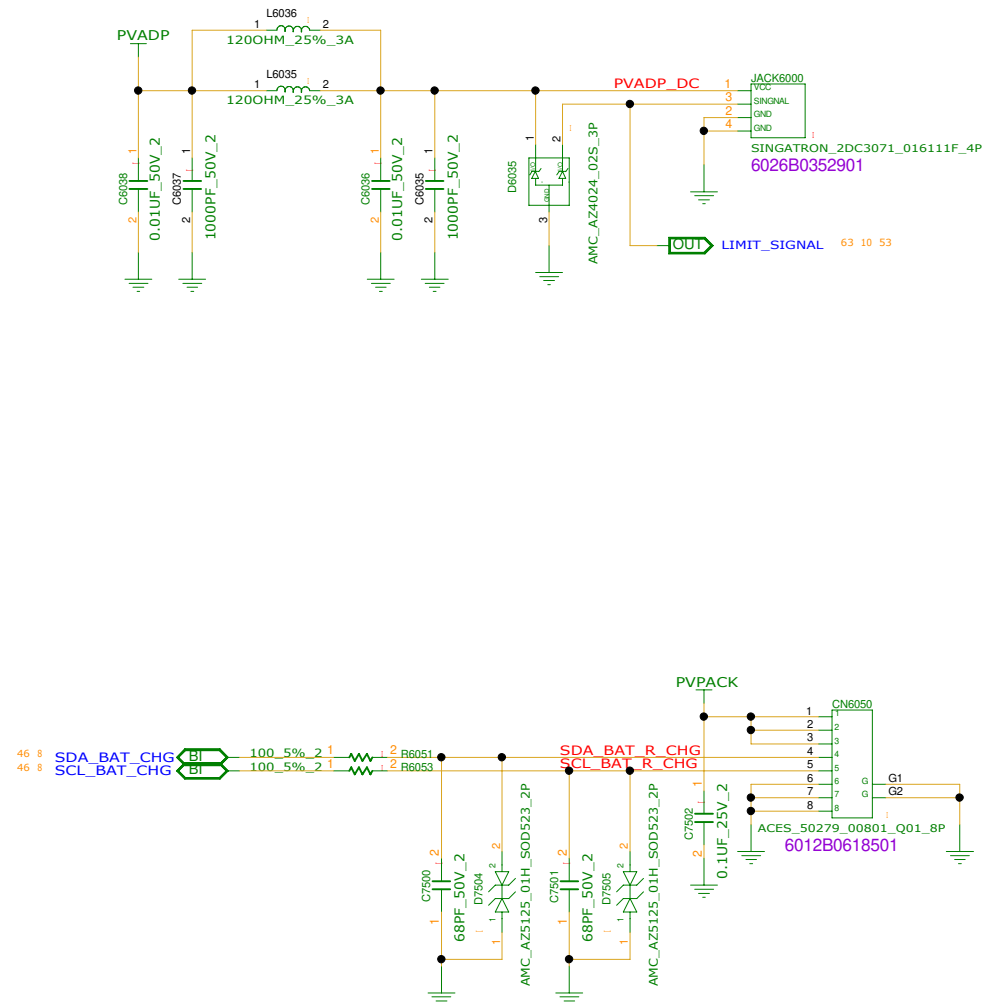


INVENTEC

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Block	Diagram

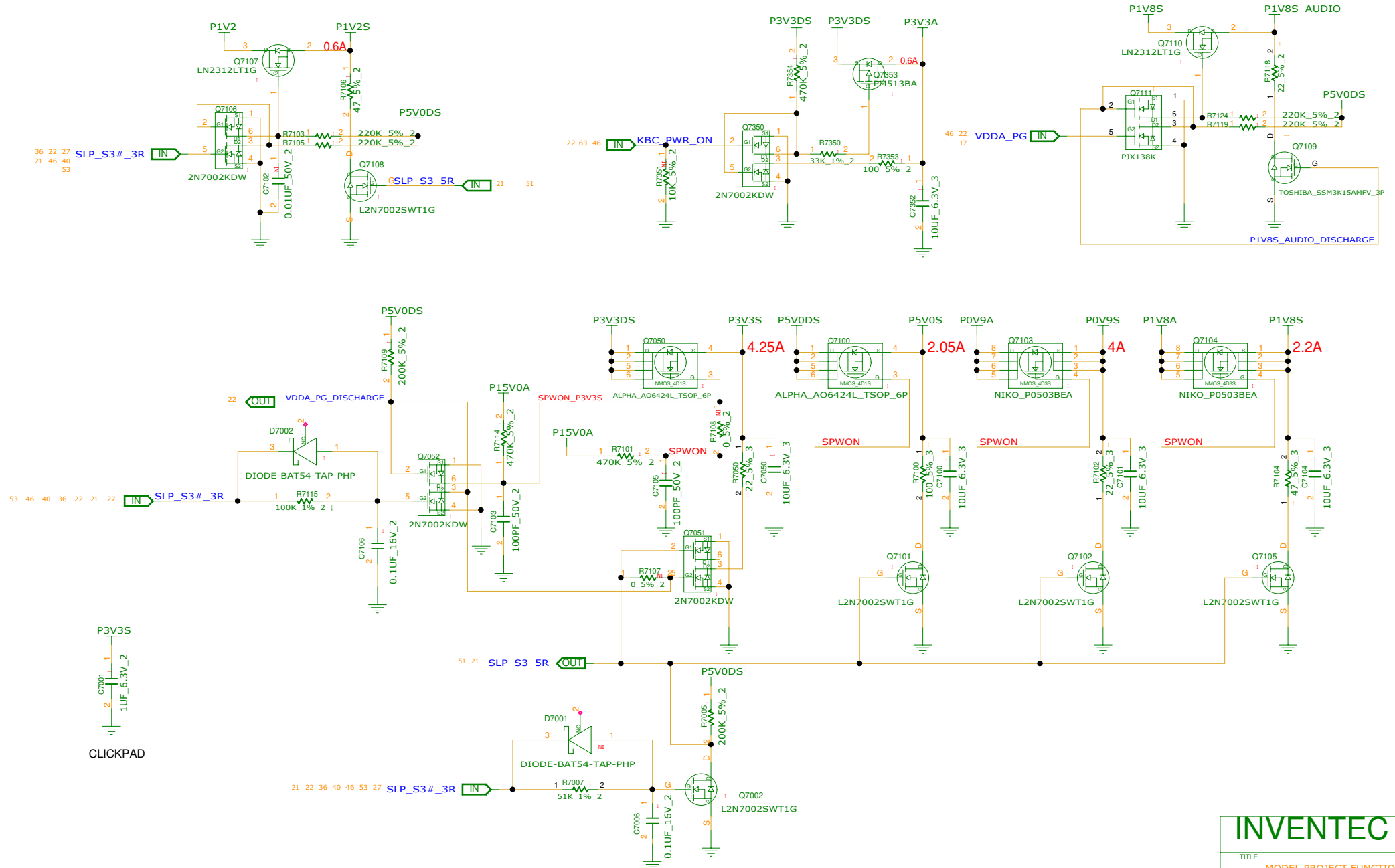
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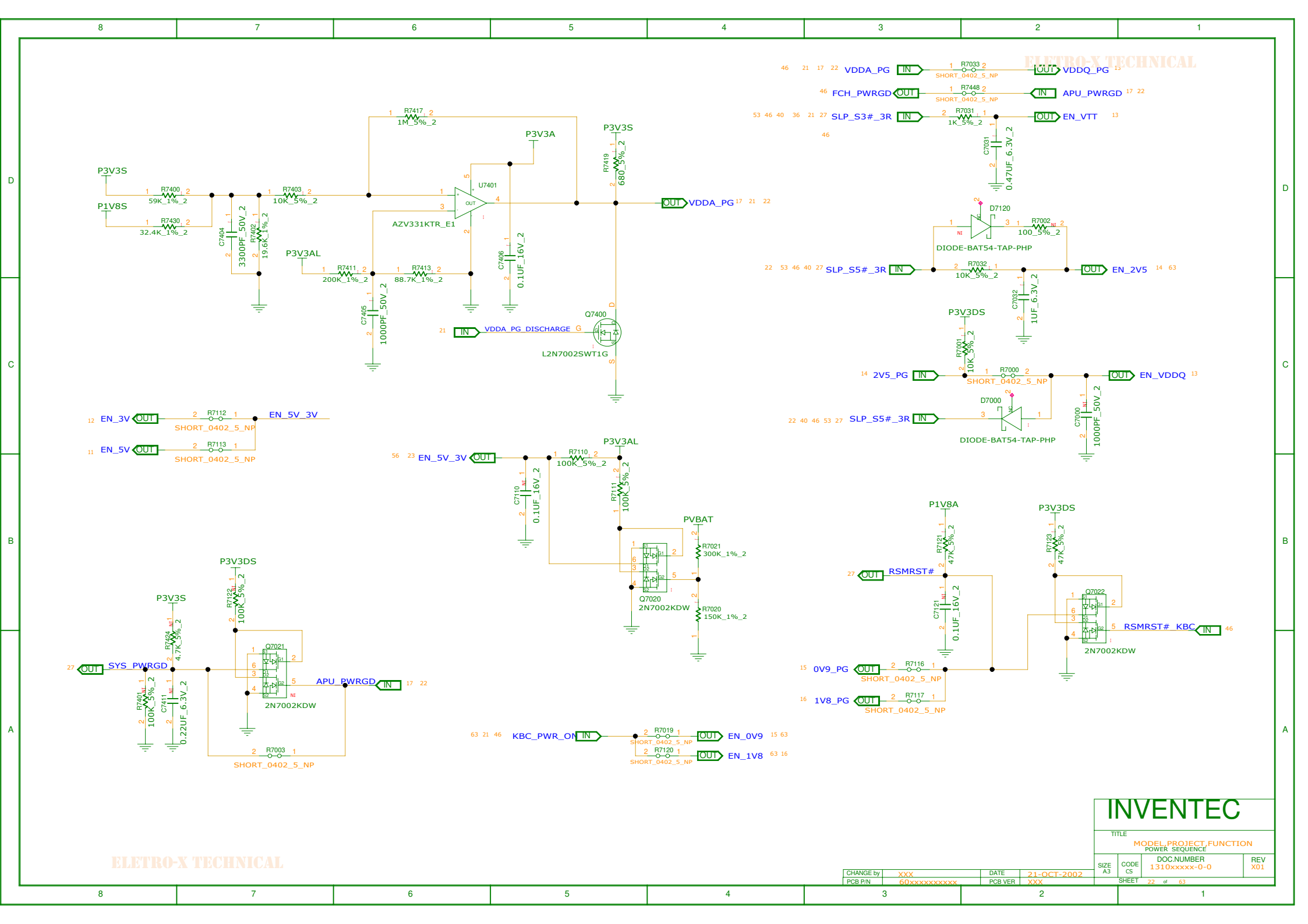
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ELETRO-X TECHNICAL

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PCB P/N	60xxxxxxxxxx	PCB VER	XXX	SIZE	A3	CODE	CS	SHEET	21 of 63



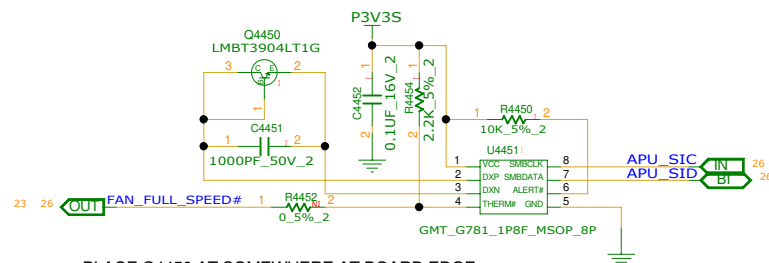
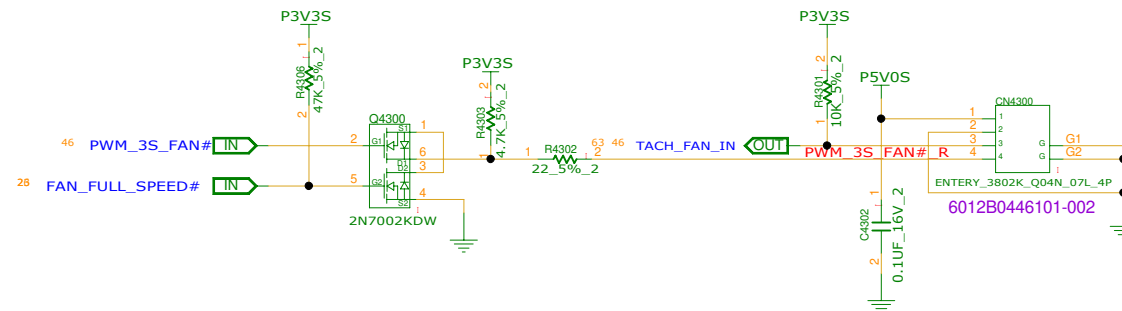
ELETRO-X TECHNICAL

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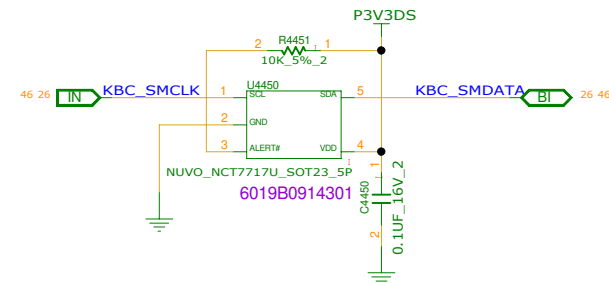
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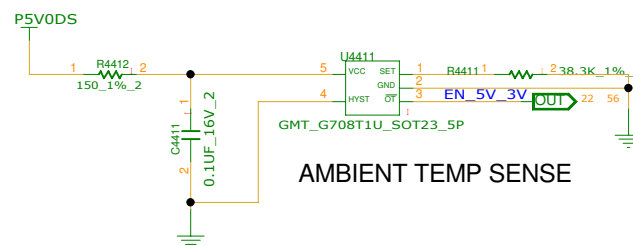


PLACE Q4450 AT SOMEWHERE AT BOARD EDGE

PLACE U4451 AT BOTTOM SIDE AND CLOSED TO CPU
BUT NOT INTERFERE WITH HIGH SPEED ROUTINGS



REFERENCE NUMBER:4450~4499



AMBIENT TEMP SENSE

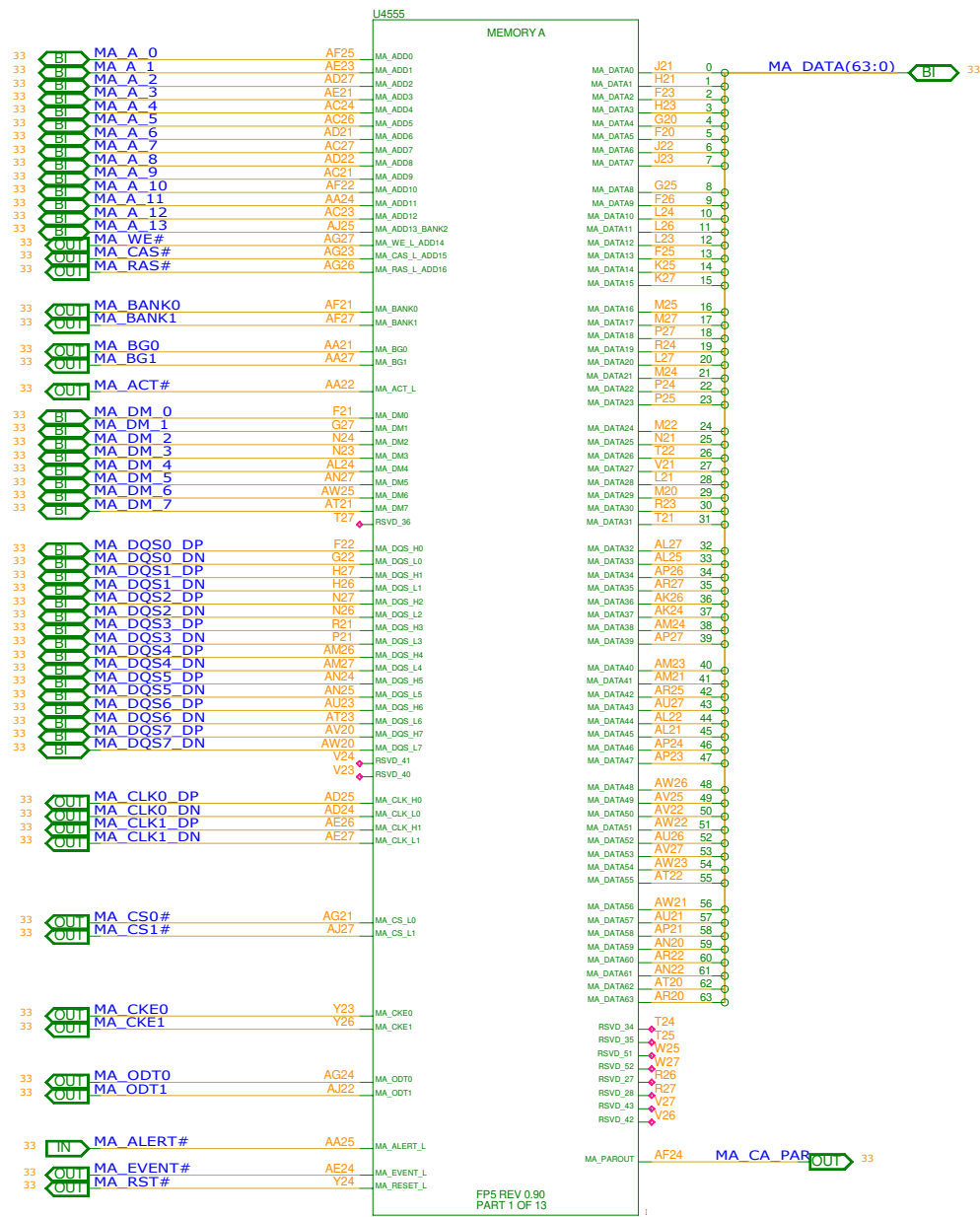
RSET (K OHM) = $0.0012T^2 - 0.9308T + 96.147$
THERMAL SHUTDOWN AT 68 DEG.

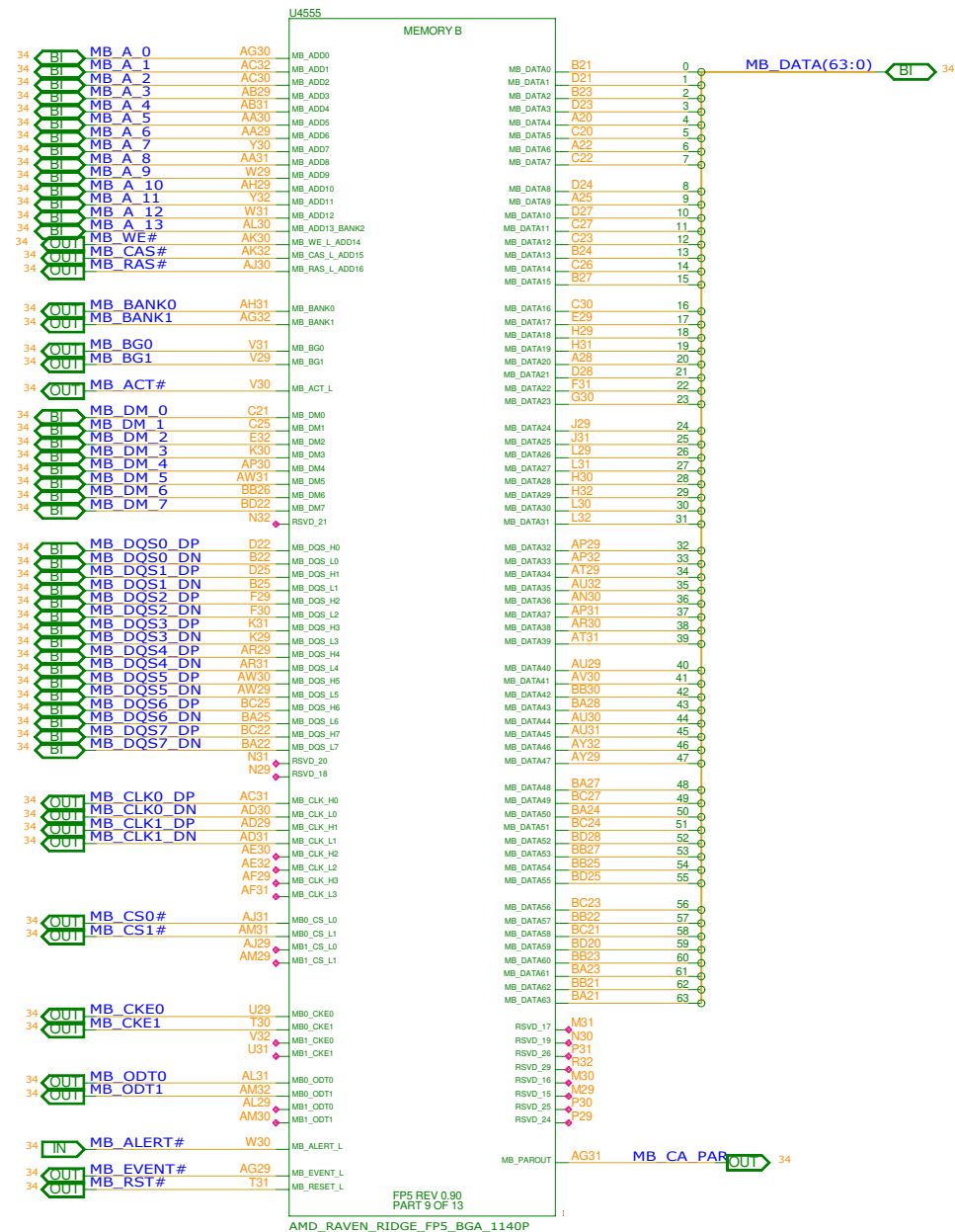
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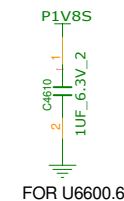
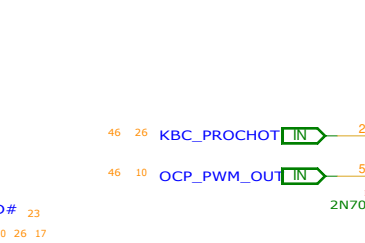
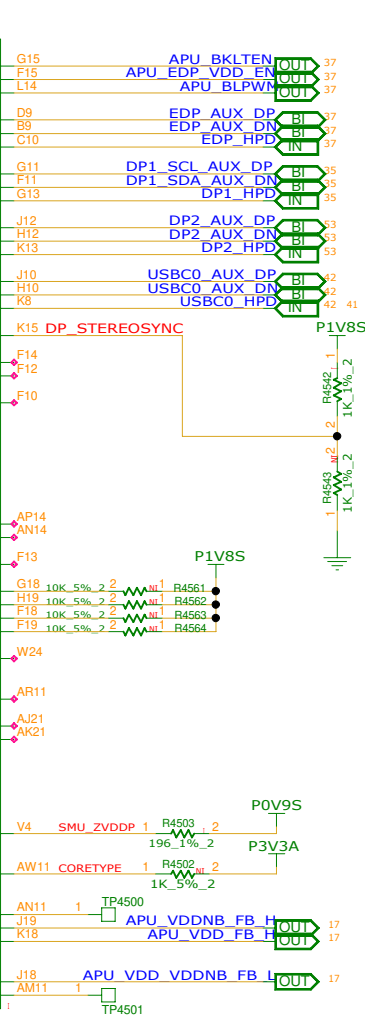
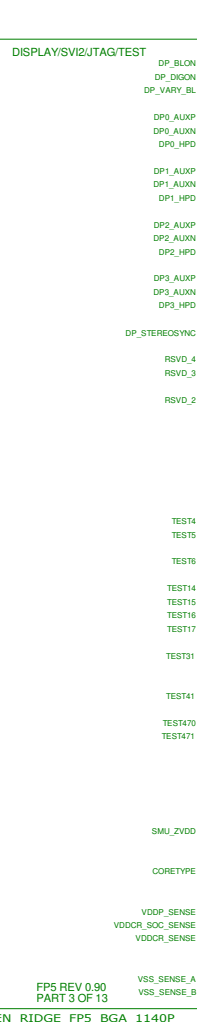
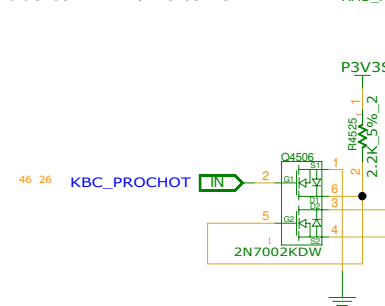
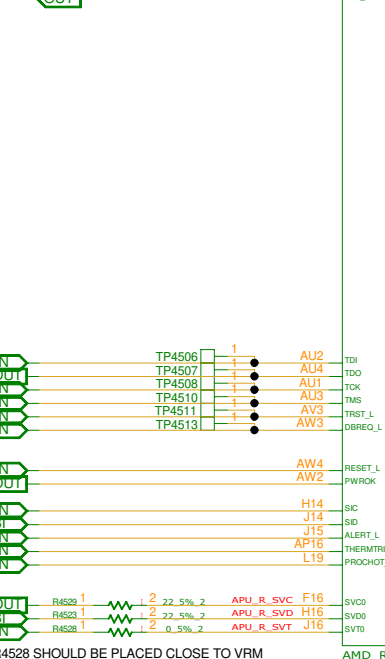
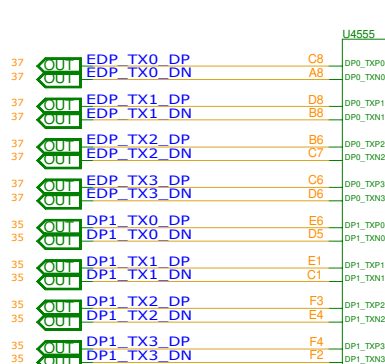
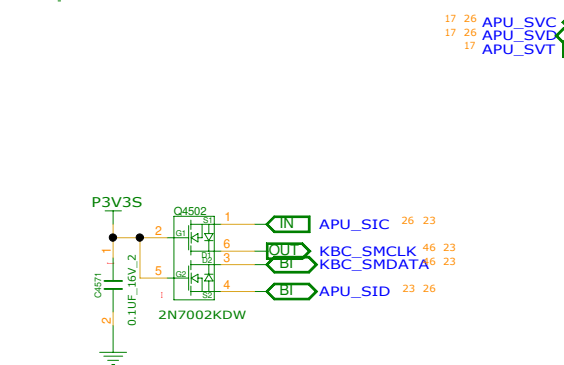
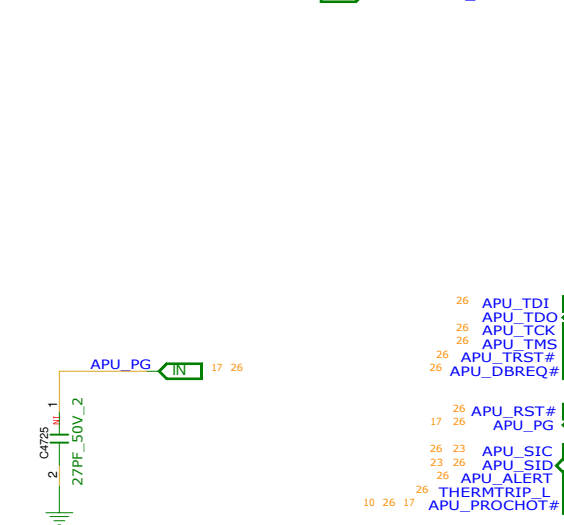
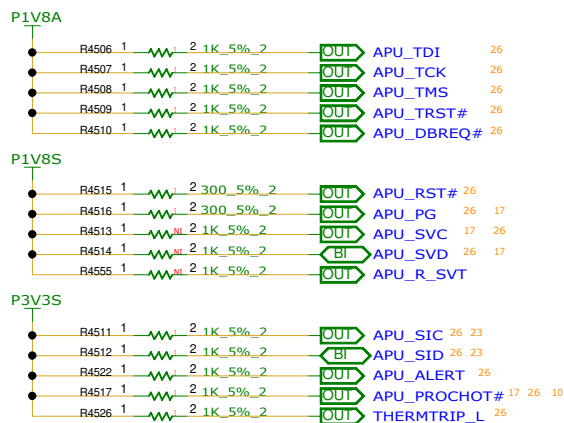
INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION FAN & THERMAL			
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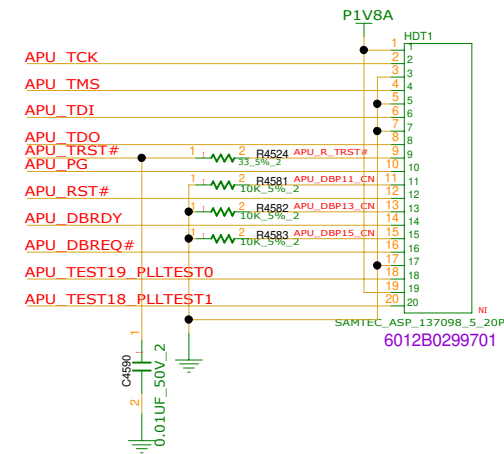
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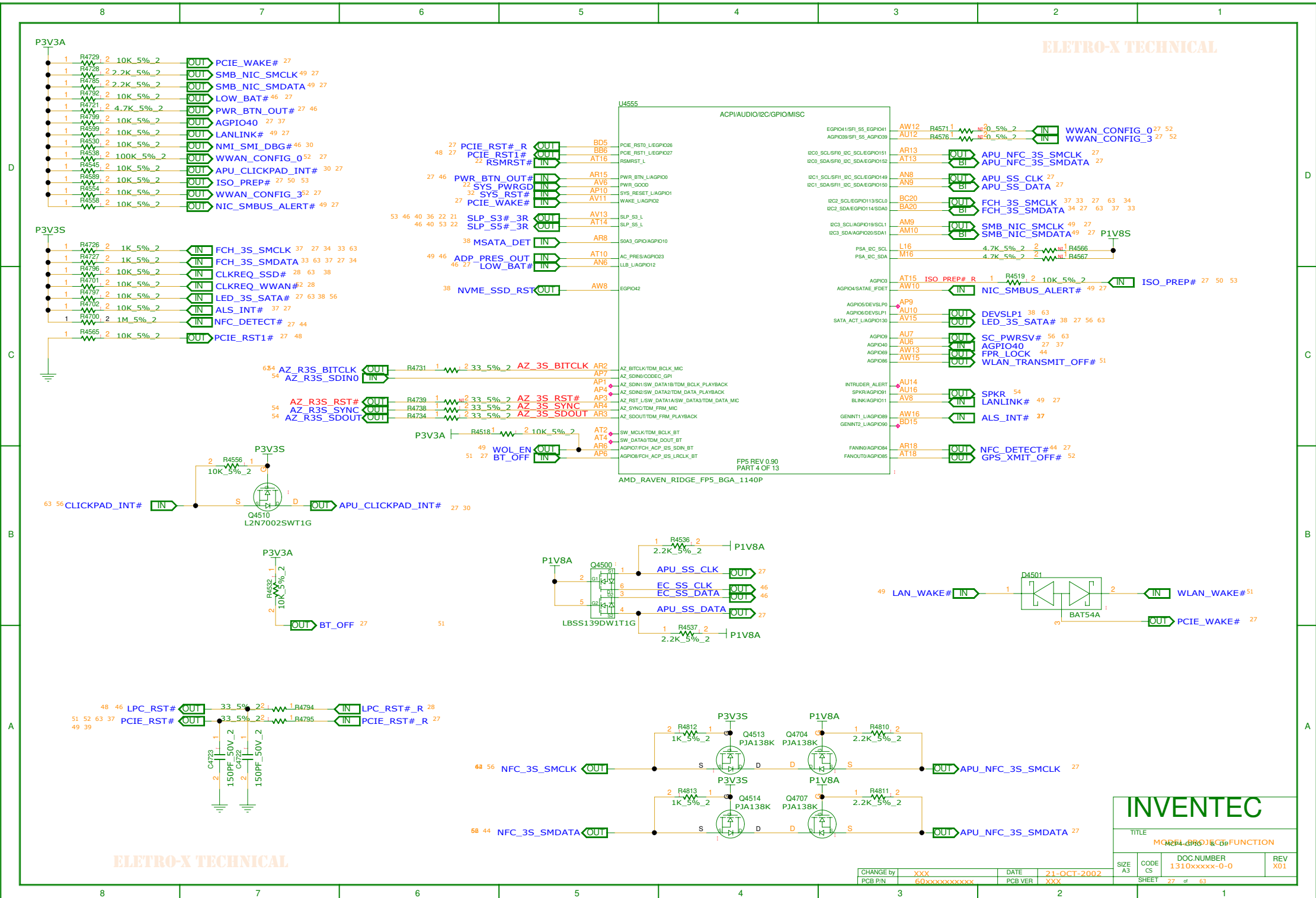


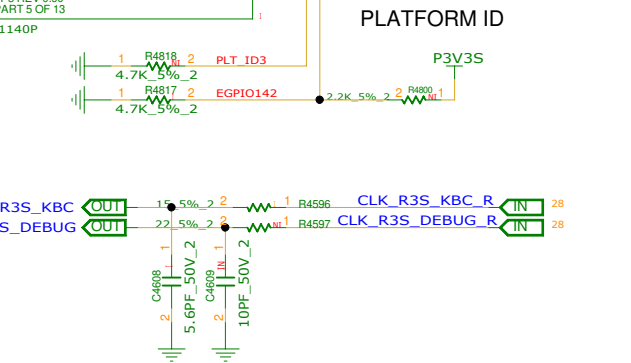
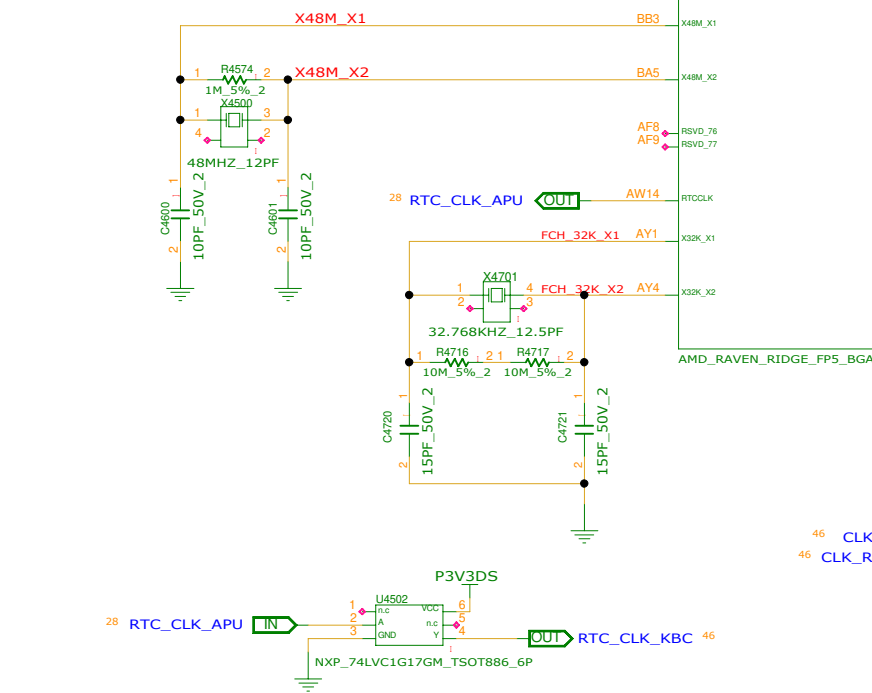
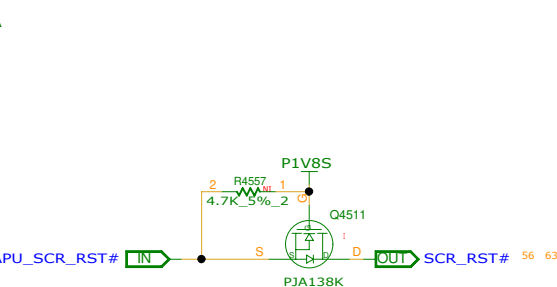
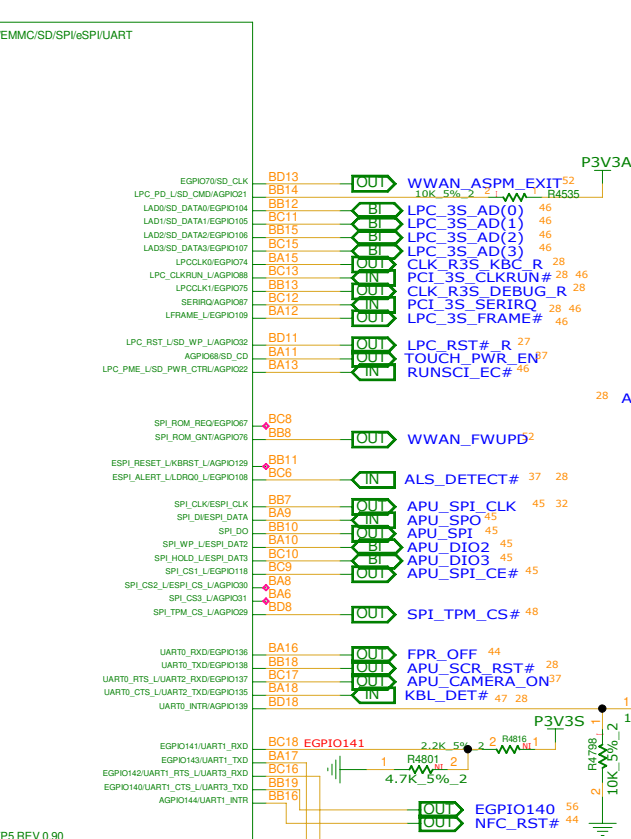
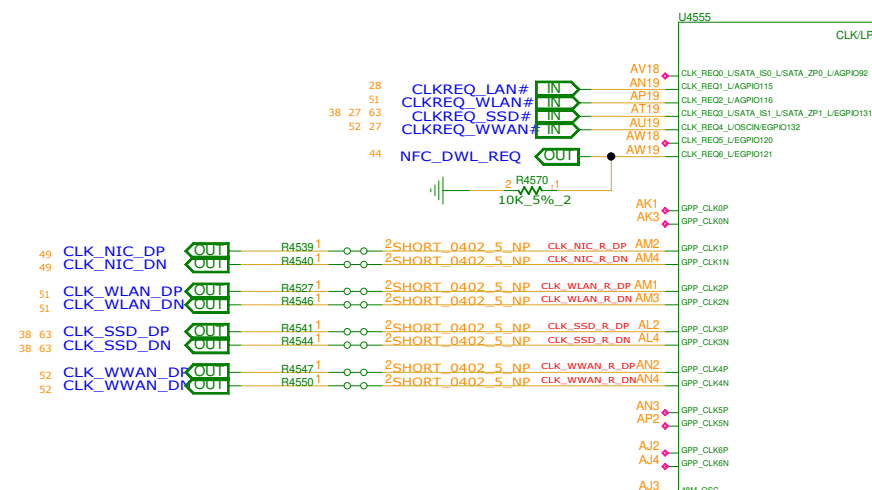
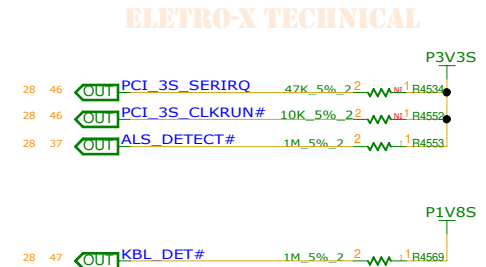
PU FOR INTERNAL
PD FOR CUSTOMER



INVENTEC

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		Block	Diagram
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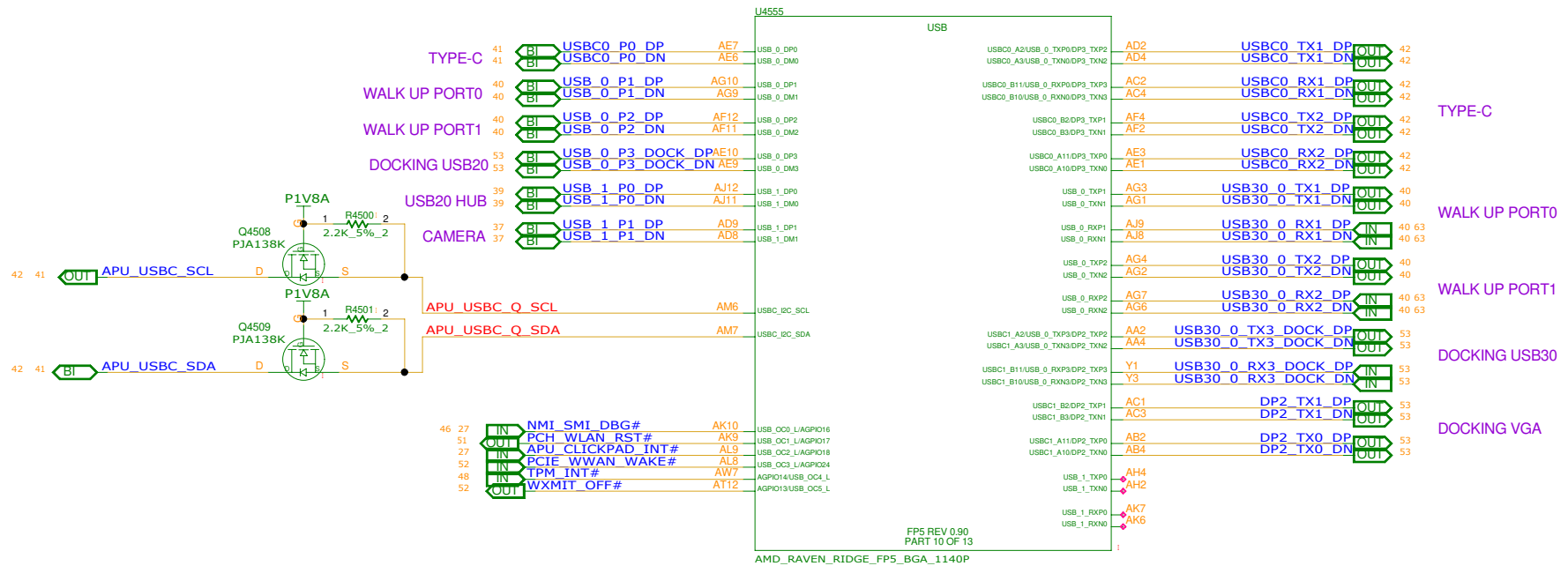


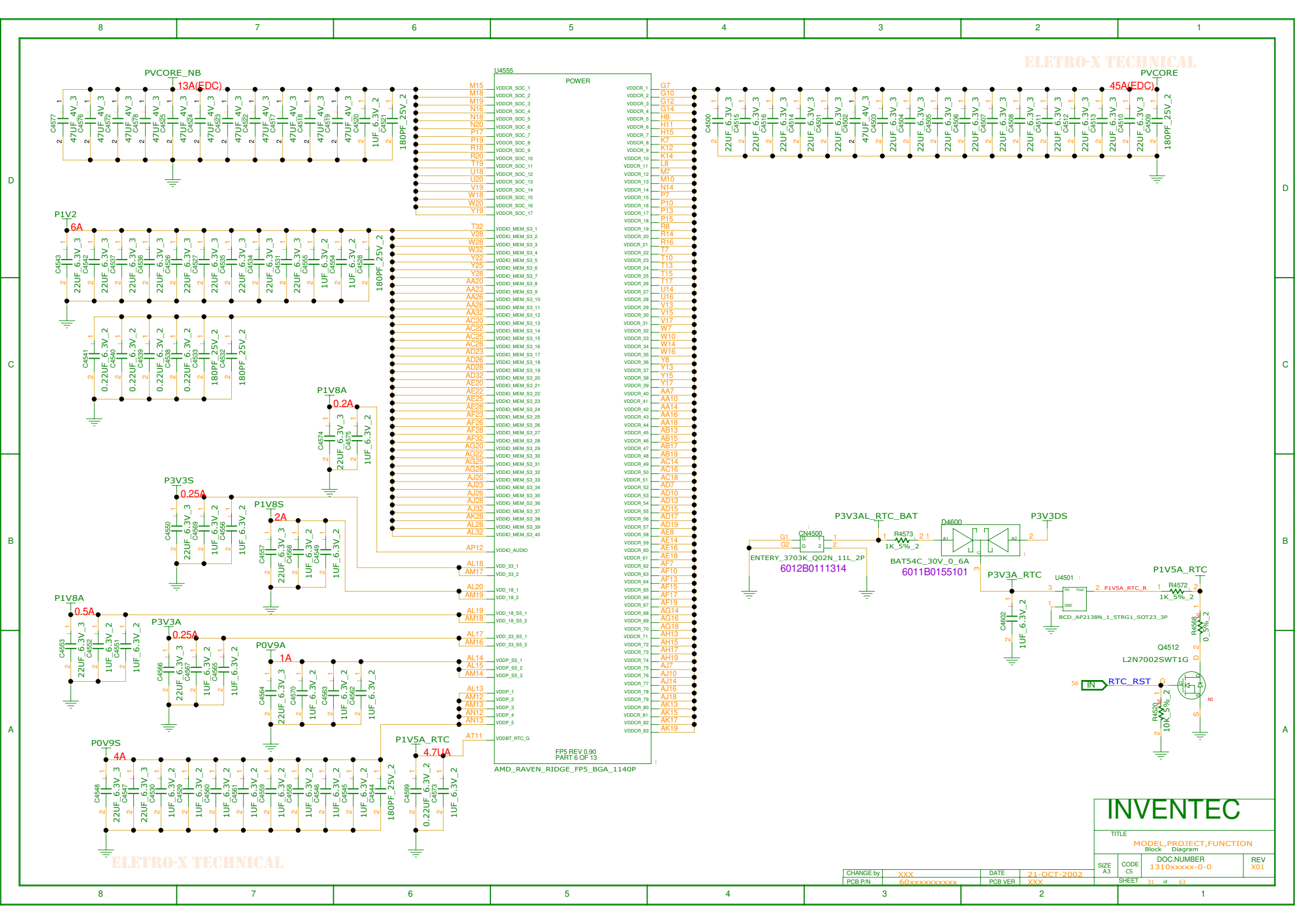
PLATFORM ID	P_ID 1 EGPIO141 INTERNAL PD	P_ID 2 EGPIO142 INTERNAL PH	P_ID 3 EGPIO143 INTERNAL PH
LLARGA13"	0	0	1
TENJIN 14"	0	1	0
TENJIN 15"	0	1	1

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION MCP5-DDR			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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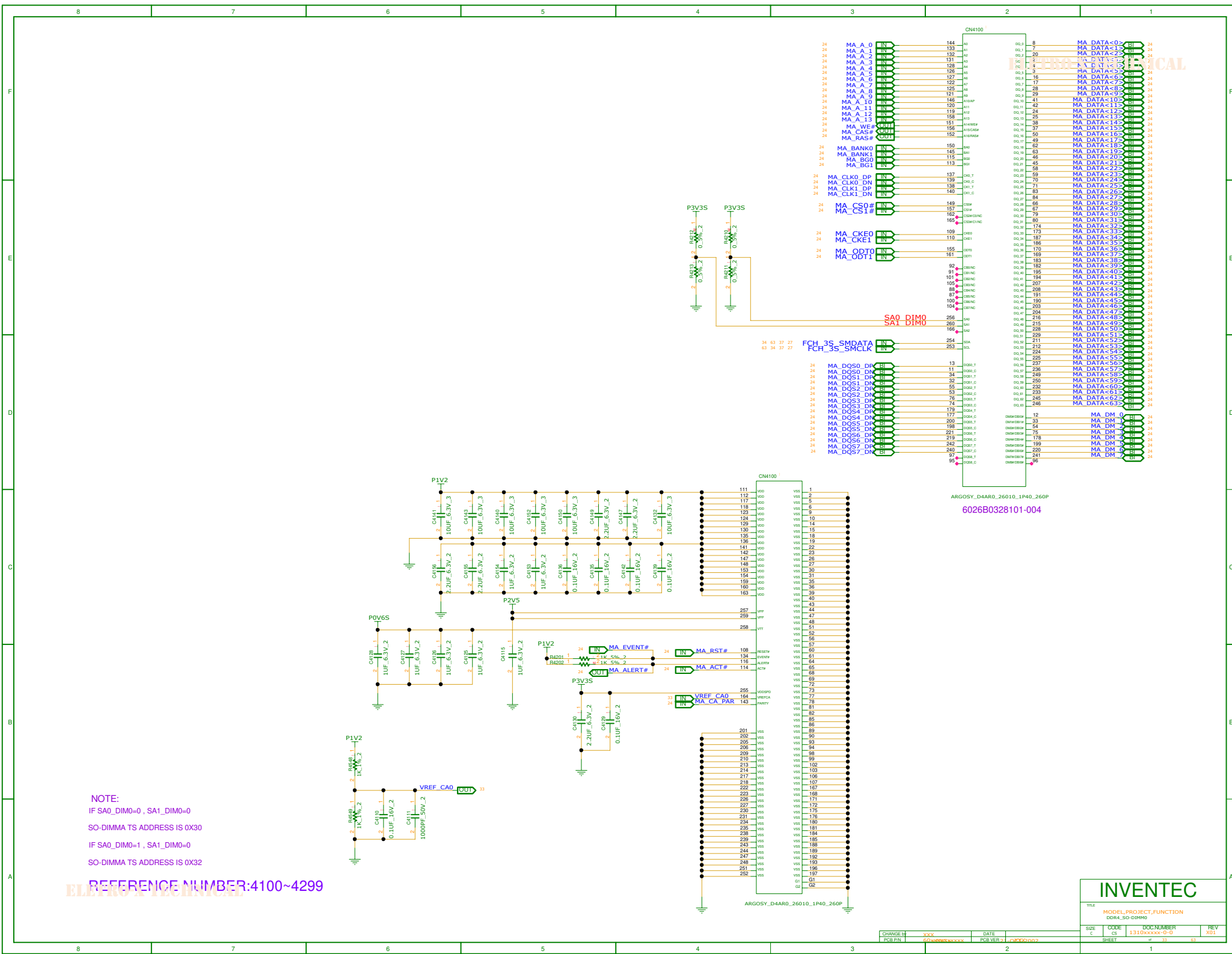




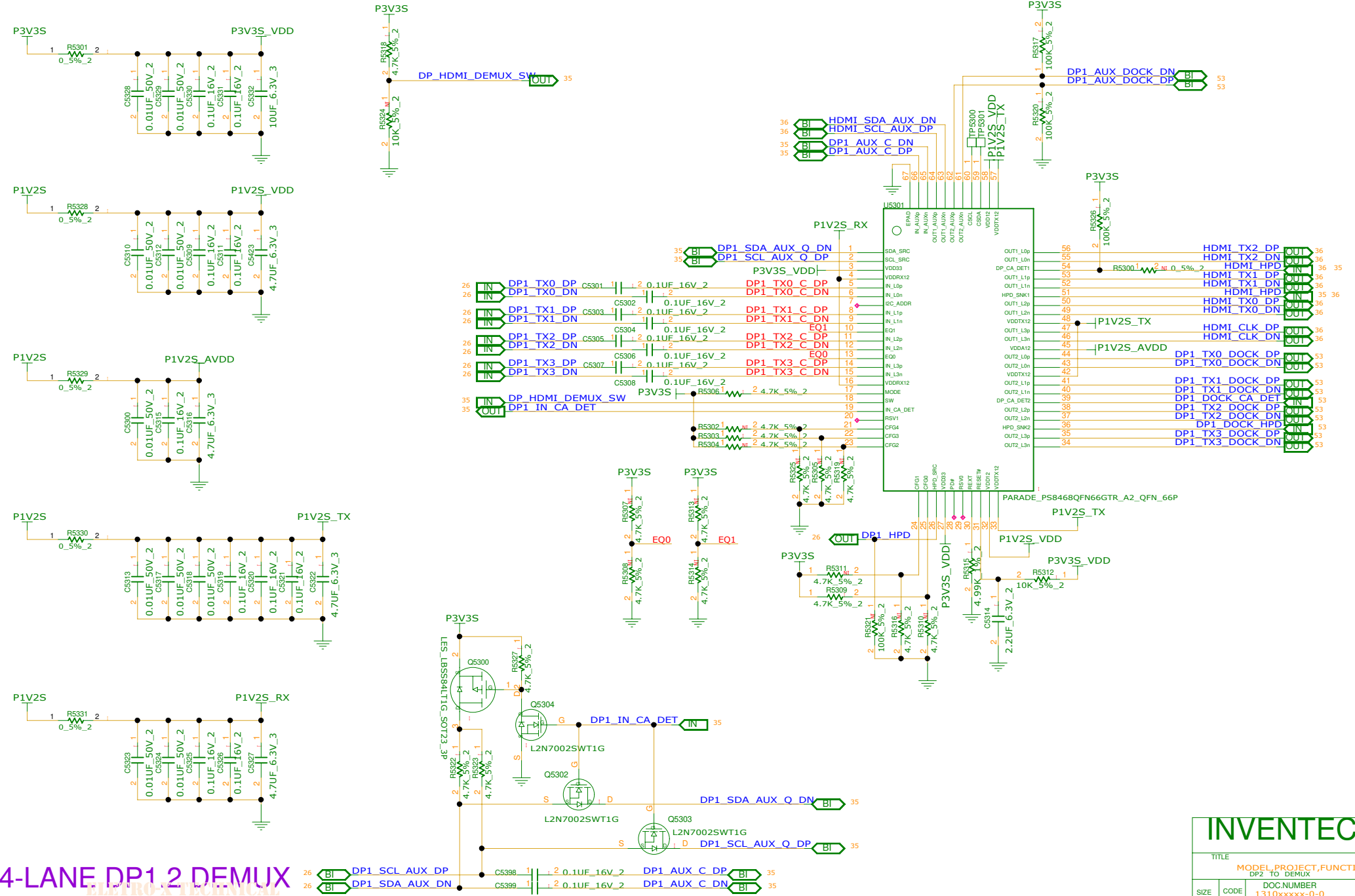


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TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
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4-LANE DP1.2 DEMUX

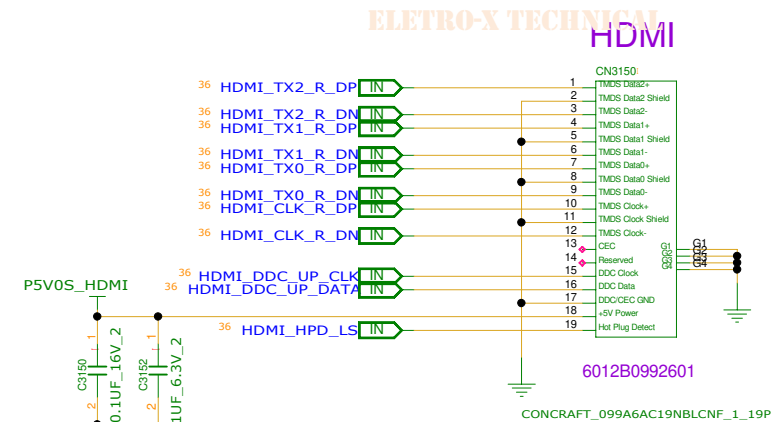
INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION DP2 TO DEMUX			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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PCB P/N	60xxxxxxxxxxx	PCB VER	XXX

53 46 40 27 SLP_S3#_3R 

HDMI_DDC_UP_CLK	1	D3150	Line-1	NC	10	HDMI_DDC_UP_CLK
HDMI_DDC_UP_DATA	2		Line-2	NC	9	HDMI_DDC_UP_DATA



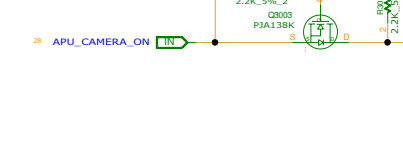
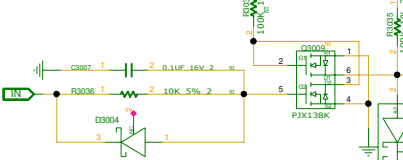
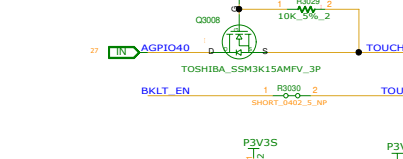
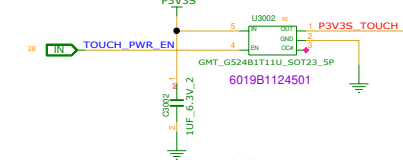
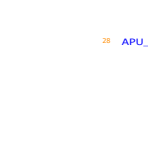
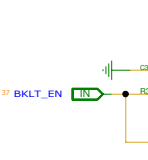
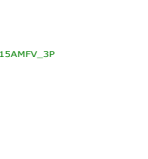
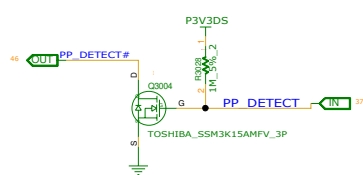
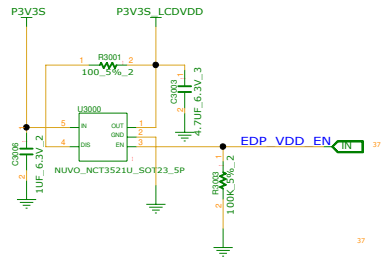
INVENTEC

TITLE	MODEL, PROJECT, FUNCTION
	LCM & WEBCAM

SIZE A3	CODE CS	DOC. NUMBER 1310xxxxx-0-0	F X
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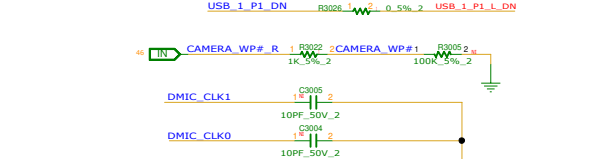
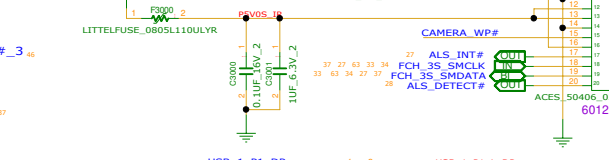
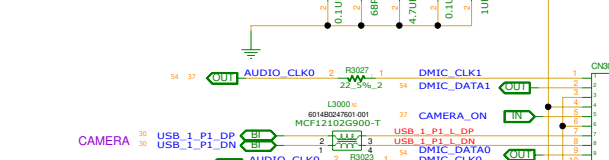
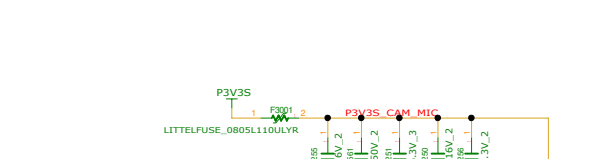
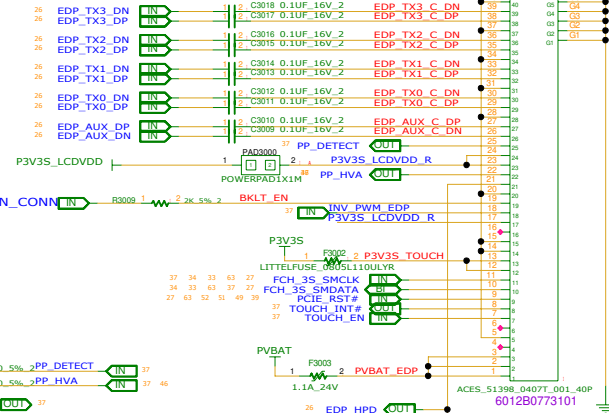
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ELETRO-X TECHNICAL

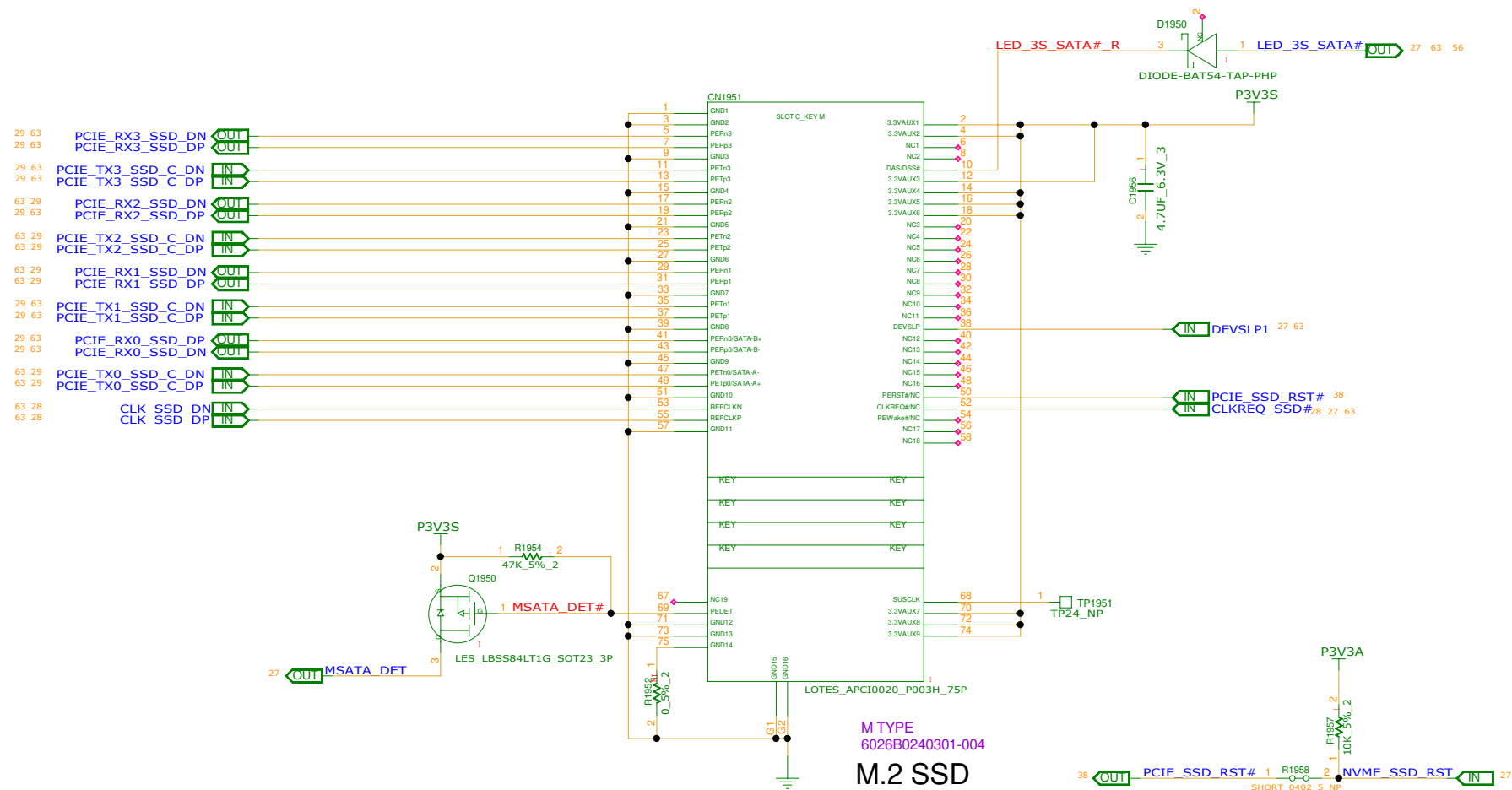


ELETRO-X TECHNICAL
DP TO EDP LEVEL SHIFT

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
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CHANGE IN	DATE	PCB VER
PCB PIN	21-OCT-2002	XXX

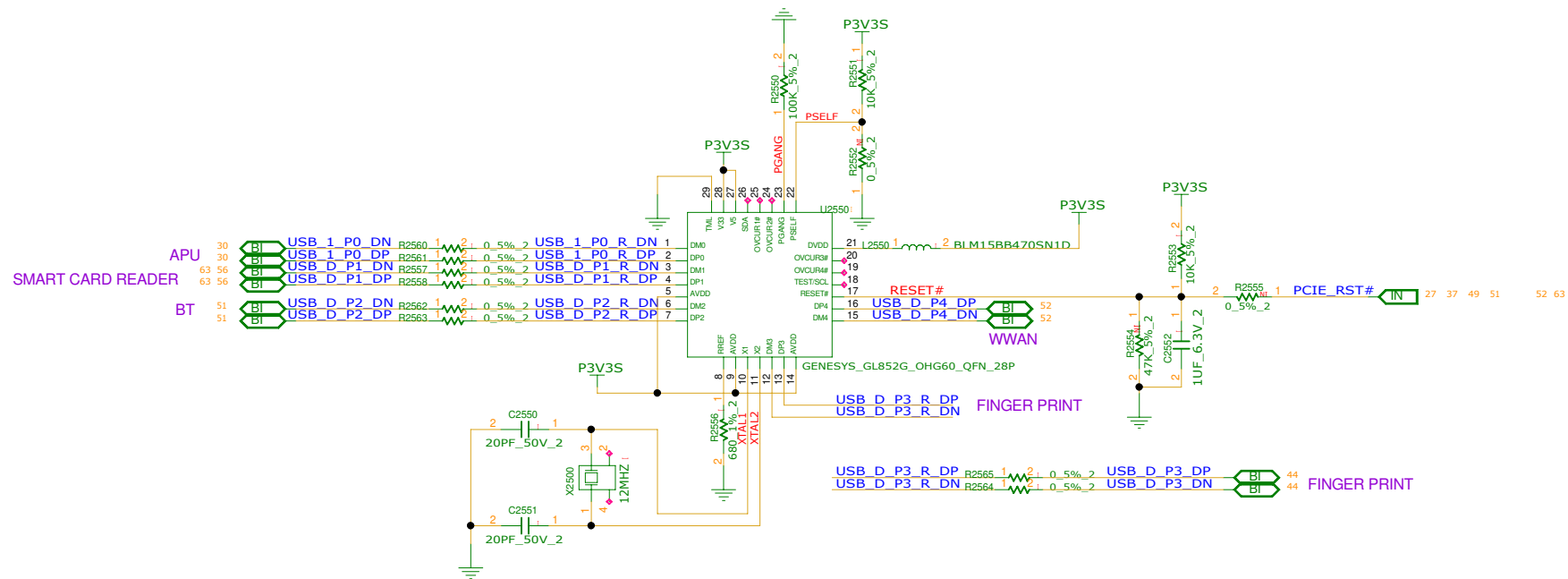
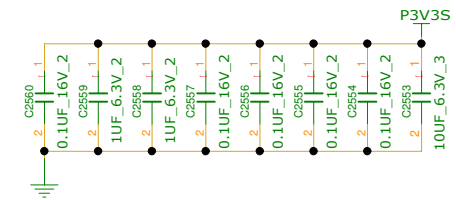


REFERENCE NUMBER:1950~1999

INVENTEC

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX

TITLE	MODEL,PROJECT,FUNCTION	DOC NUMBER	REV
SIZE	CODE	1310xxxxx-0-0	X01
A3	CS		
SHEET	38 of 63		

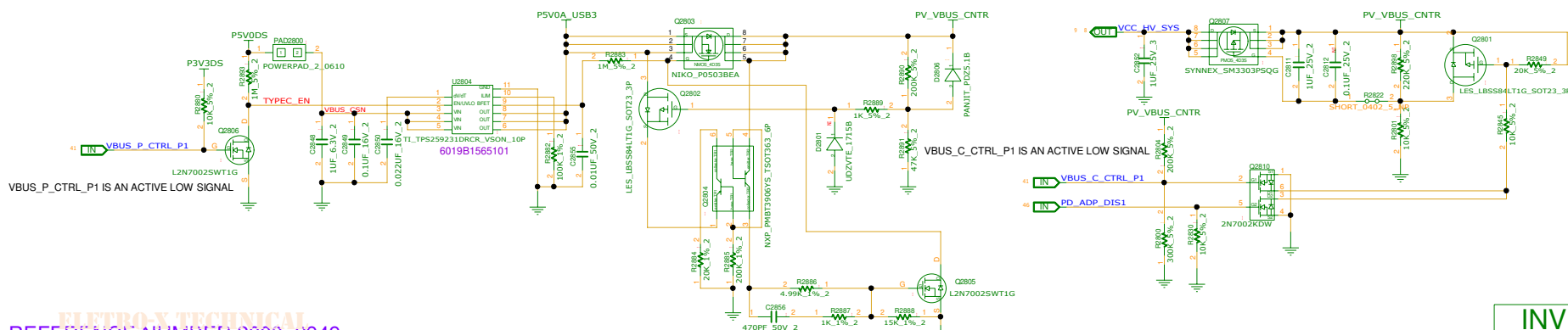
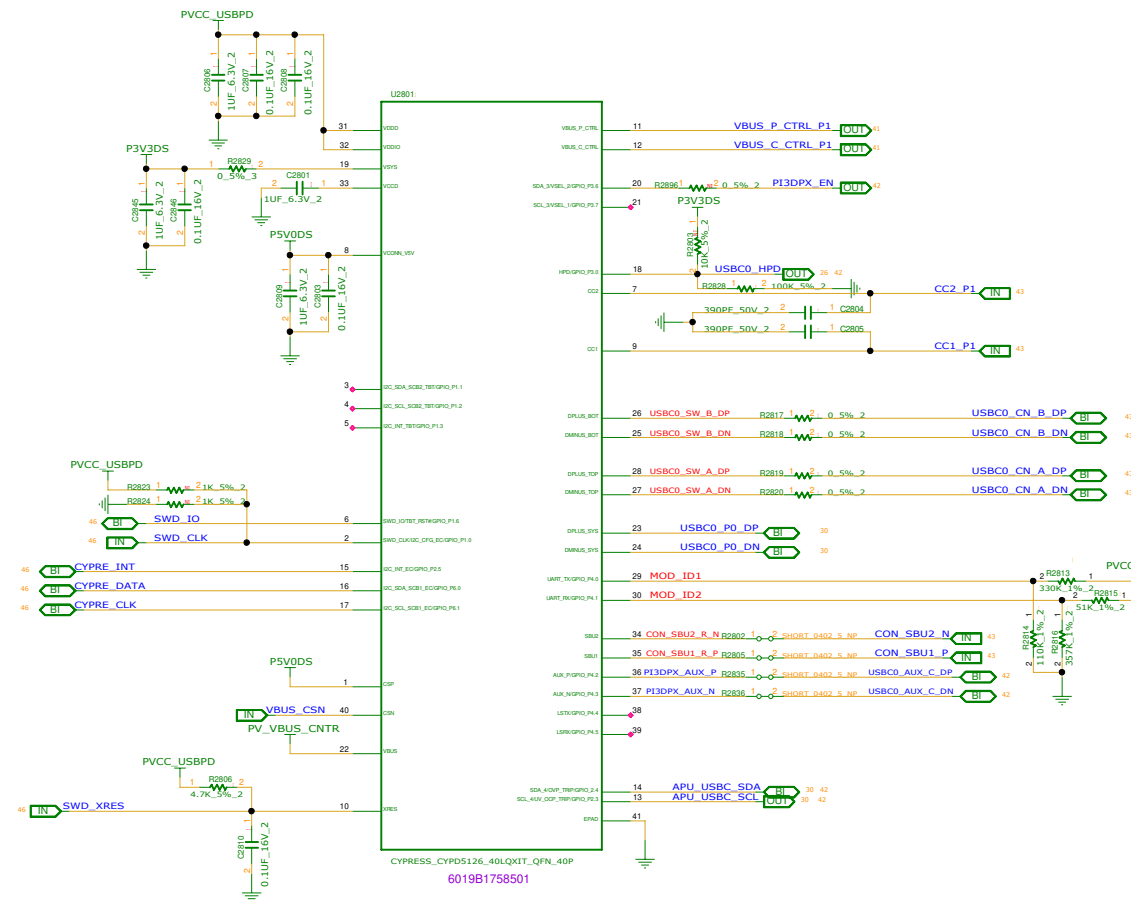


REFERENCE NUMBER: 2550~2599

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION USB20 HUB			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
SHEET 39 of 63			

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX



REFERENCE NUMBER:2800~2949

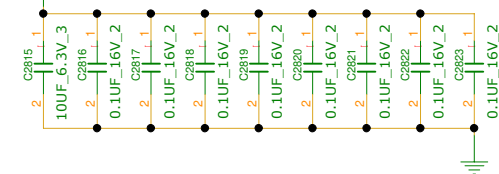
USB-C PD CONTROLLER

INVENTEC

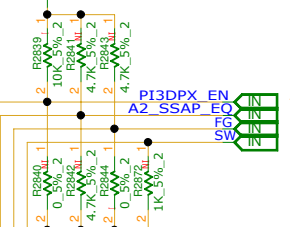
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310XXXX-0-0	201
SHEET	#	#	
41	#	83	

CHANGES	DATE
PCB PIN XXXXXXXXXXXXXXXX	21-OCT-2002
PCB VER	XXX

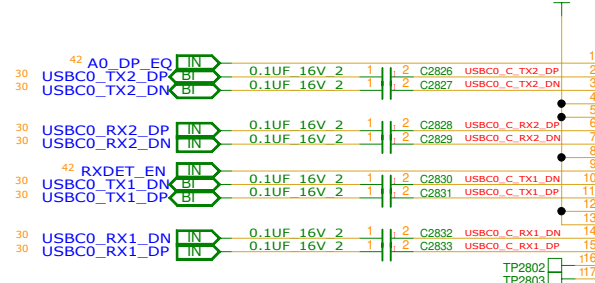
P3V3DS



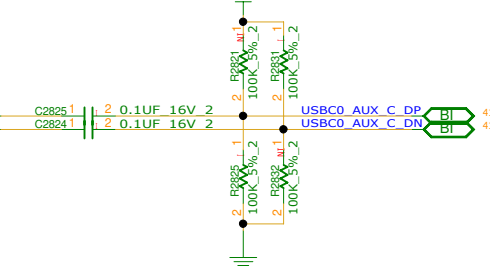
P3V3DS



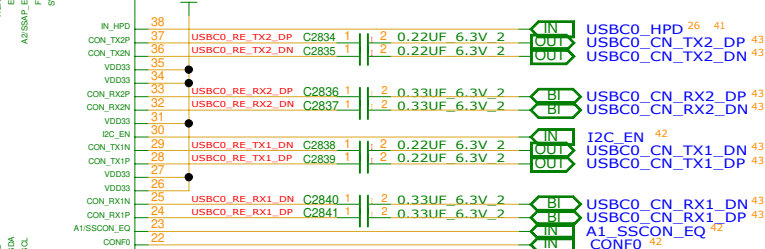
P3V3DS



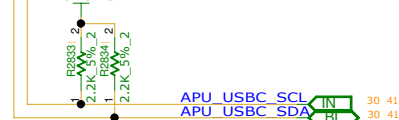
P3V3DS



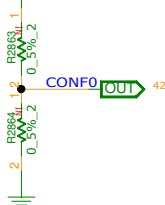
P3V3DS



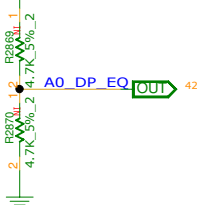
P3V3DS



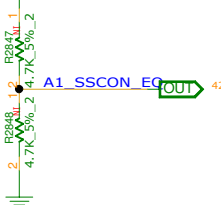
P3V3DS



P3V3DS



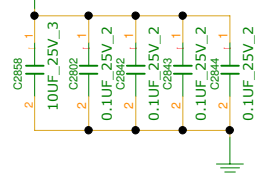
P3V3DS



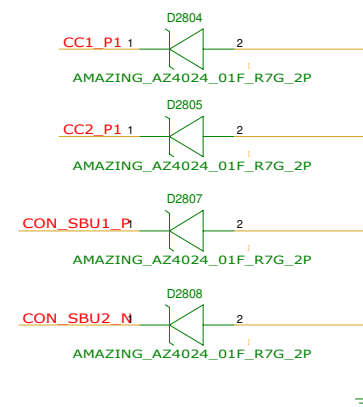
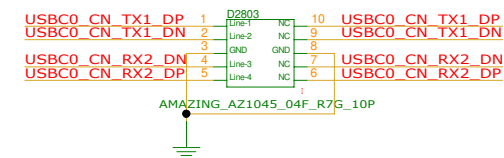
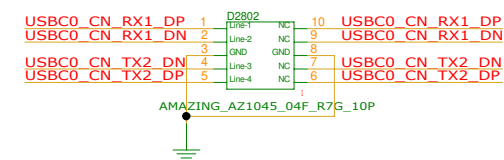
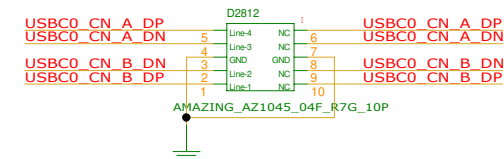
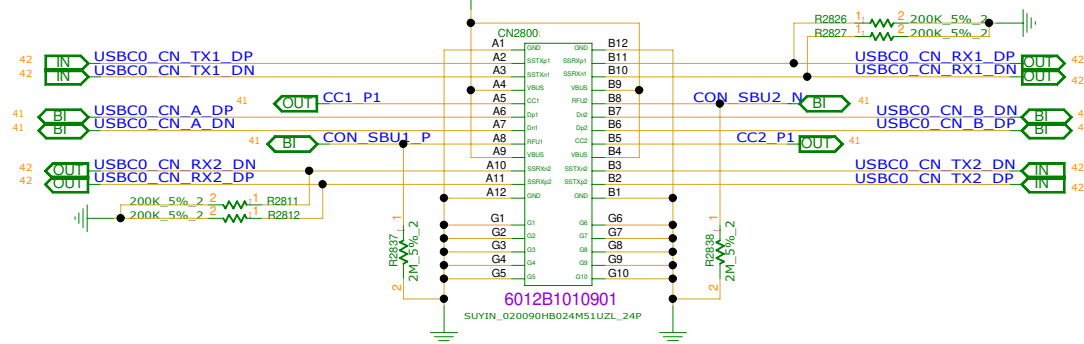
RXDET_EN

I2C_EN

PV_VBUS_CNTR



PV_VBUS_CNTR



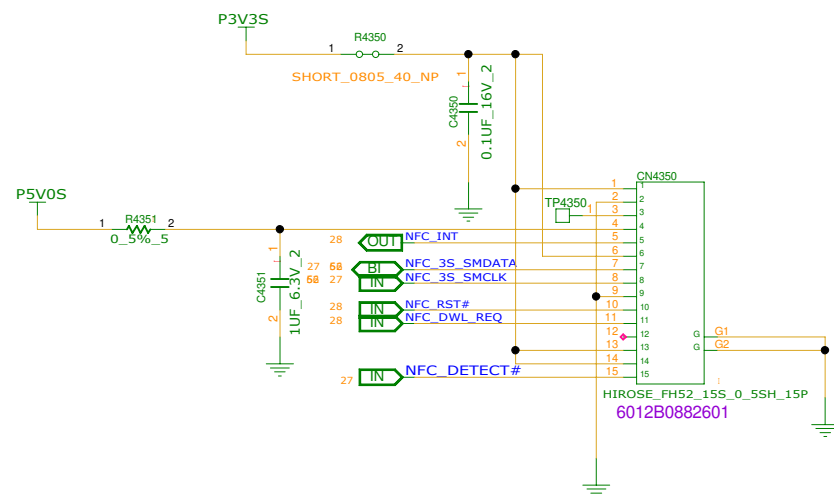
USB TYPE C

REFERENCE NUMBER:2800 ~ 2949

INVENTEC

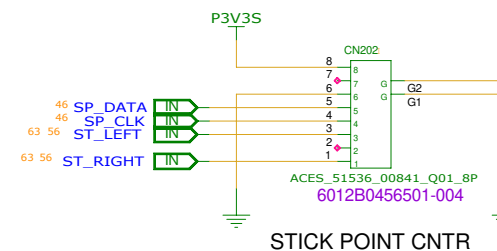
TITLE			
MODEL,PROJECT,FUNCTION			
USB TYPE-C CNTR			
SIZE	CODE	DOC.NUMBER	F
A3	CS	1310xxxx-0-0	
SHEET 43 of 63			

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX



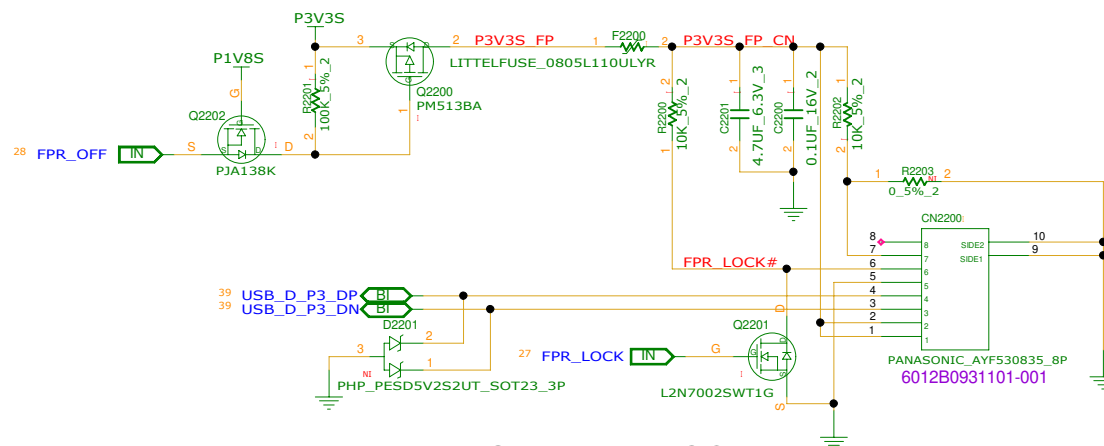
NFC MODULE CNTR

REFERENCE NUMER : 4350~4399



STICK POINT CNTR

REFERENCE NUMER : 200~299

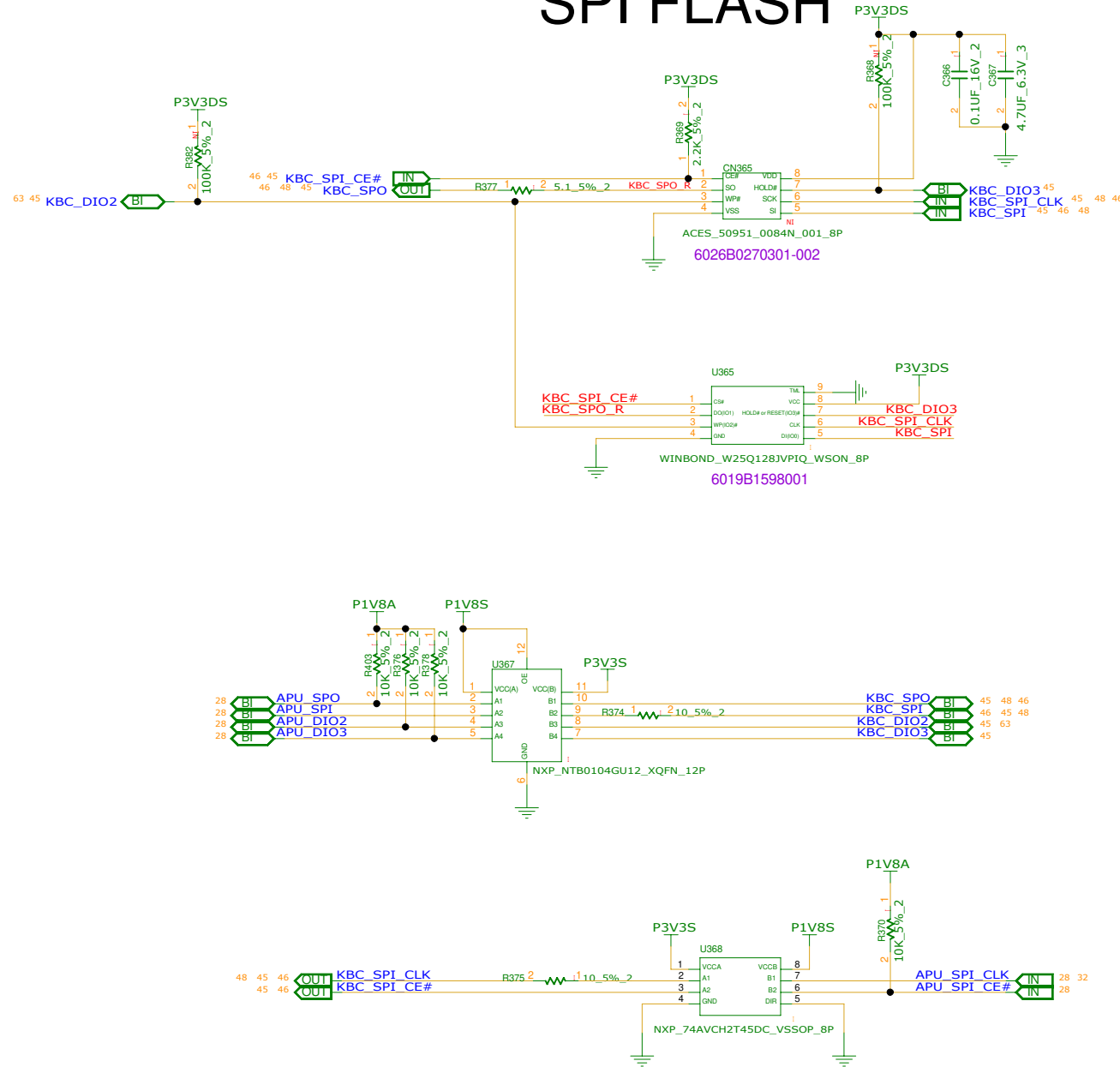


FINGER PRINT CONN

REFERENCE NUMER : 2200~2249

SPI FLASH

ELETRO-X TECHNICAL



INVENTEC

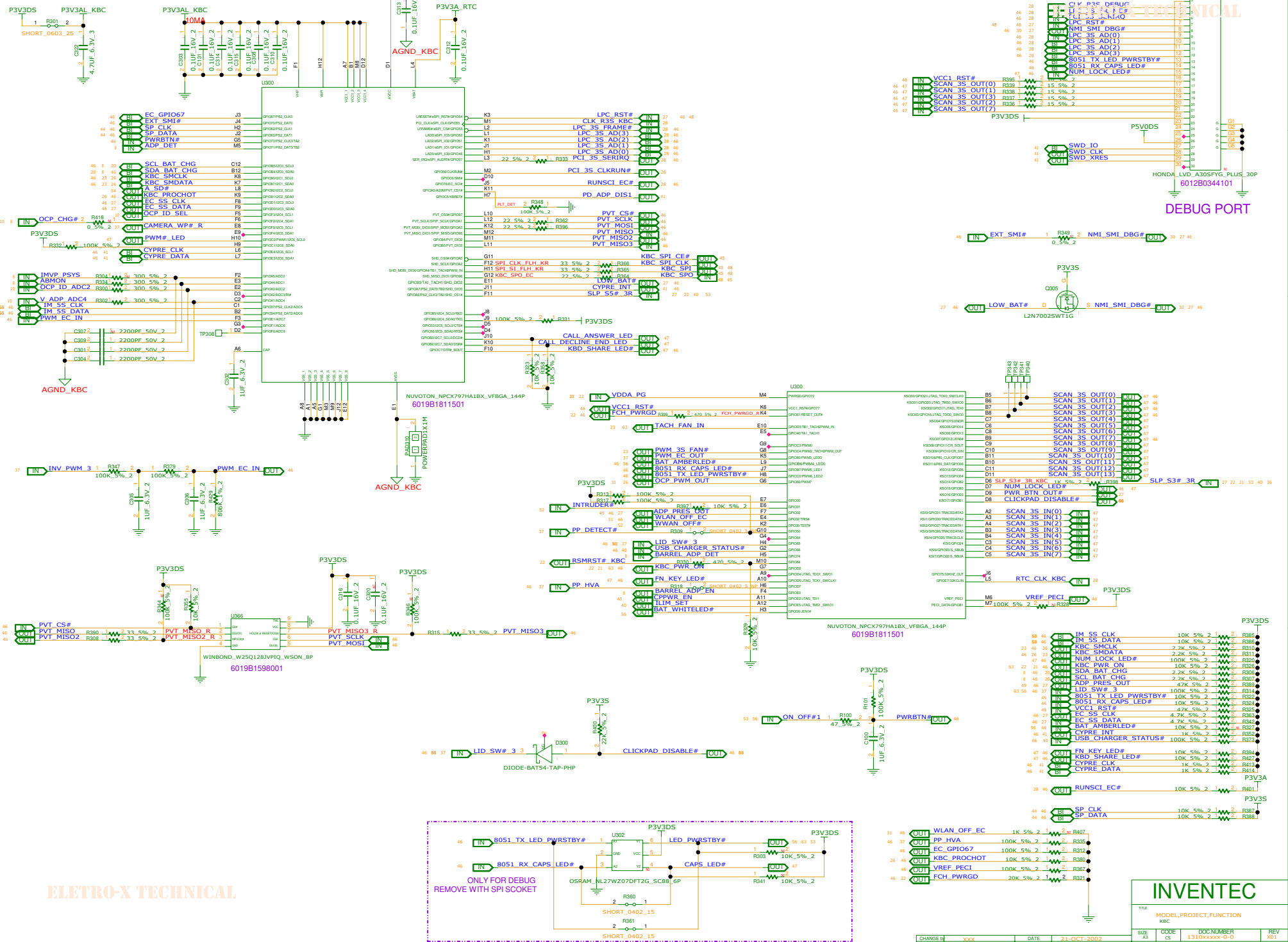
TITLE
MODEL,PROJECT,FUNCTION
BIOS

SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET		45 of 63	

CHANGE by	DATE
XXX	21-OCT-2002
PCB P/N	PCB VER
60xxxxxxxxxx	XXX

ELETRO-X TECHNICAL

REFERENCE NUMBER : 300~399



INVENTEC

MODEL,PROJECT,FUNCTION

SIZE CODE DOCNUMBER REV

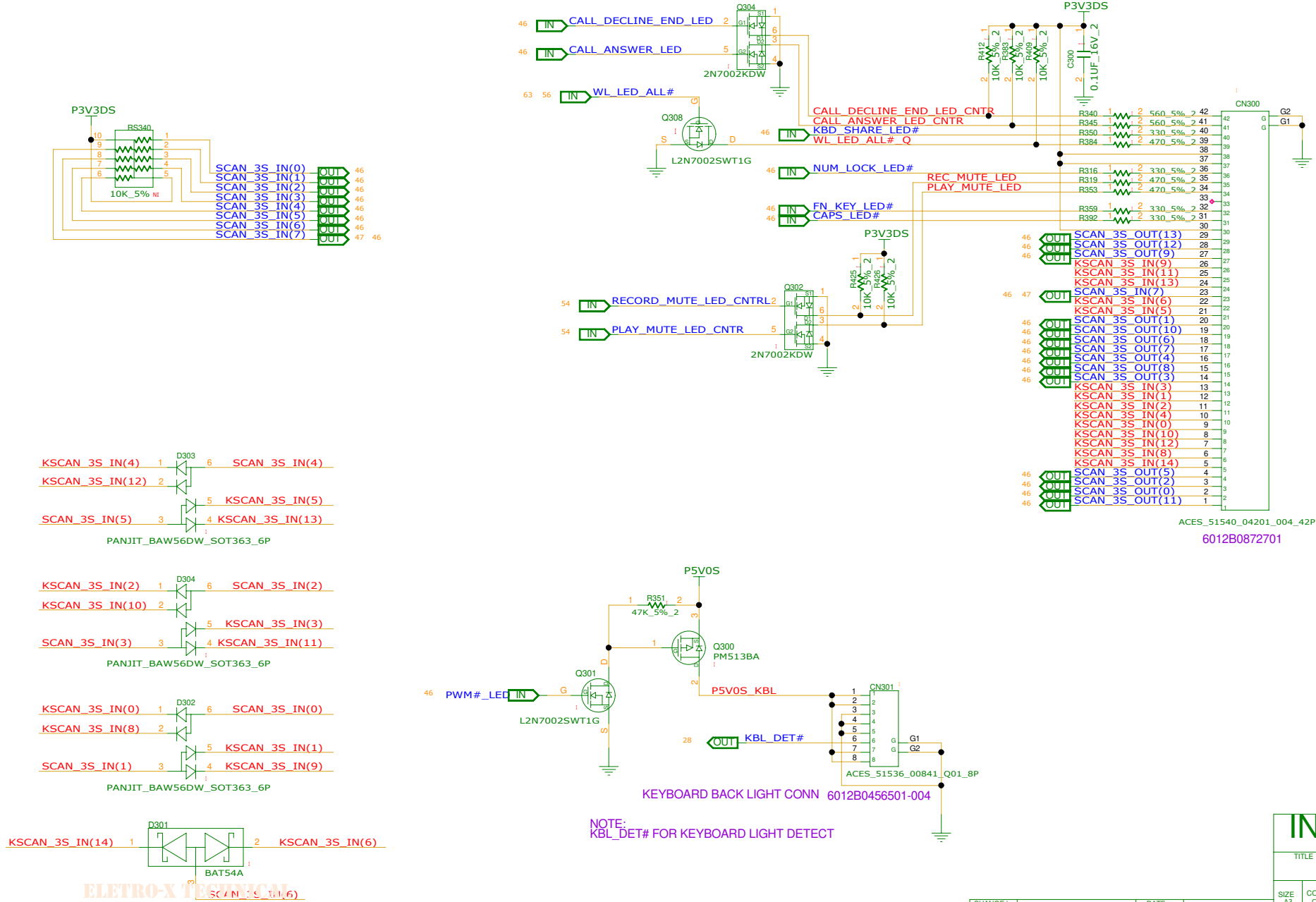
A3 CS 1310100000-0-0

SHEET 46 of 83

CHANGE PCB PIN XXXXXXXXXXXX DATE 21-OCT-2002 PCB VER XXX

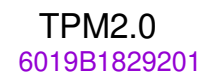
REFERENCE NUMER : 300~399

ELETR-X TECHNICAL



INVENTEC			
TITLE			
MODE KEYBOARD FUNCTION			
SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01
SHEET 47 of 63			

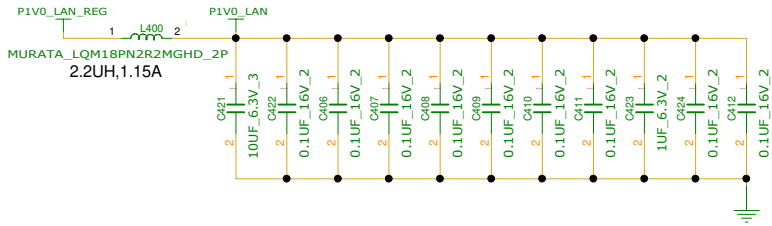
CHANGE by XXX	DATE 21-OCT-2002
PCB P/N 60xxxxxxxxxx	PCB VER XXX



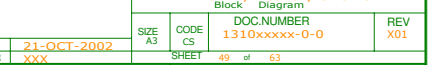
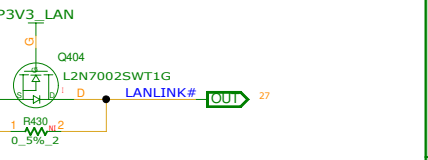
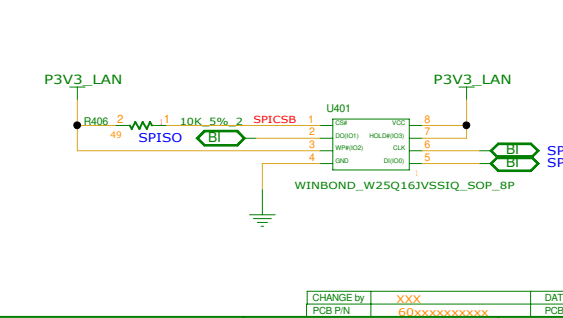
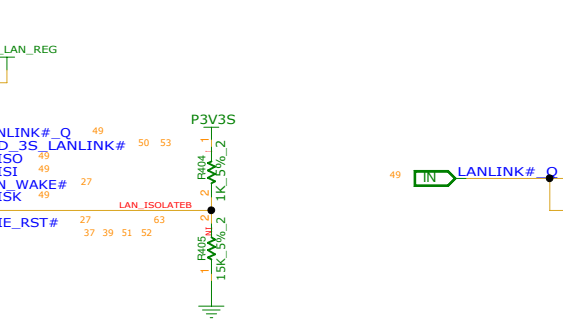
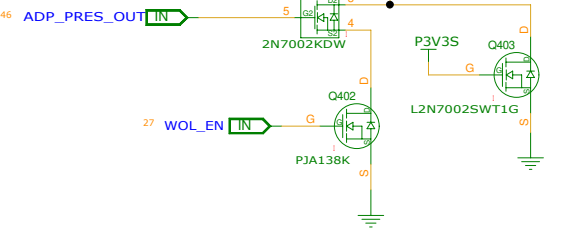
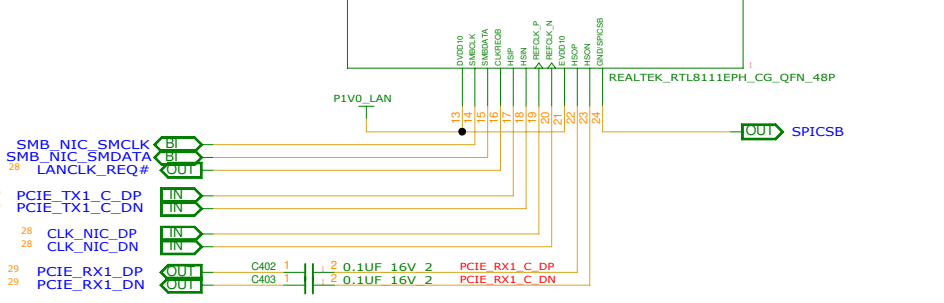
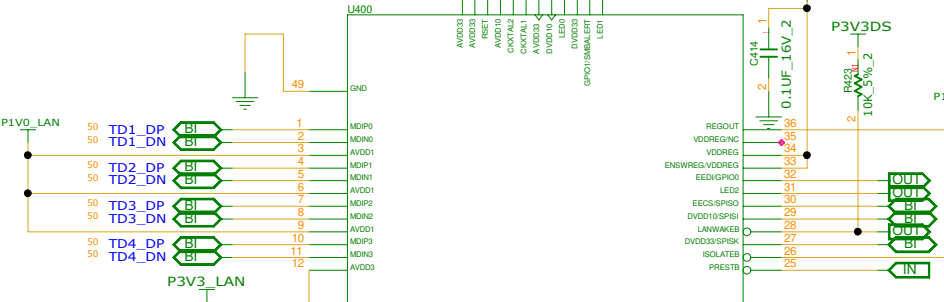
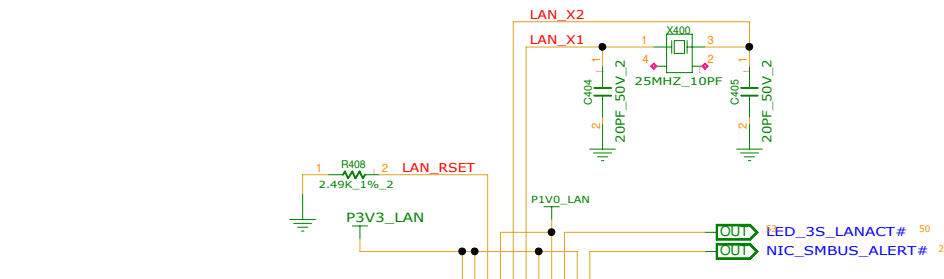
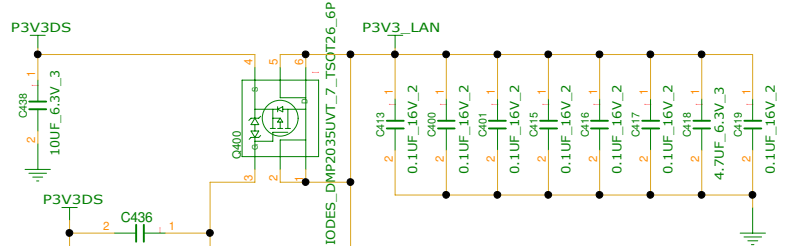
INVENTEC

SIZE	CODE	DOC.NUMBER	R
		1212-0000-00	

SIZE A3	CODE CS	1310xxxxx-0-0	X
SHEET		48 of 63	



P3V3_LAN RISING TIME 0.5MS~100MS



REFERENCE NUMER : 400~469

INVENTEC

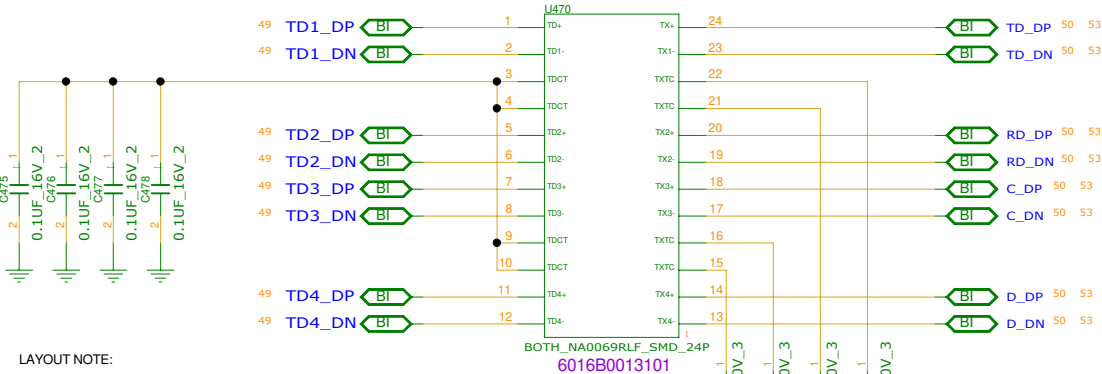
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 49 of 63			

CHANGE by	XXX	DATE	21-OCT-2002
PCB PIN	60xxxxxxxxxx	PCB VER	XXX

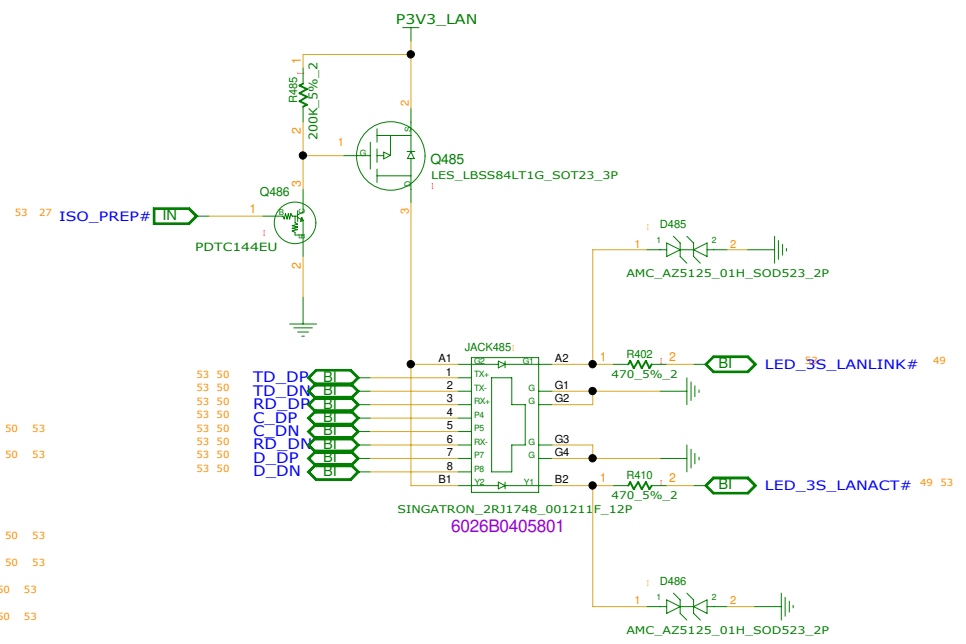
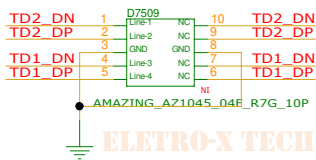
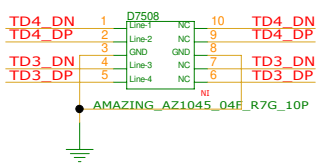
TRANSFORMER & RJ45 CONN

ELETRO-X TECHNICAL

REFERENCE NUMER : 470~499



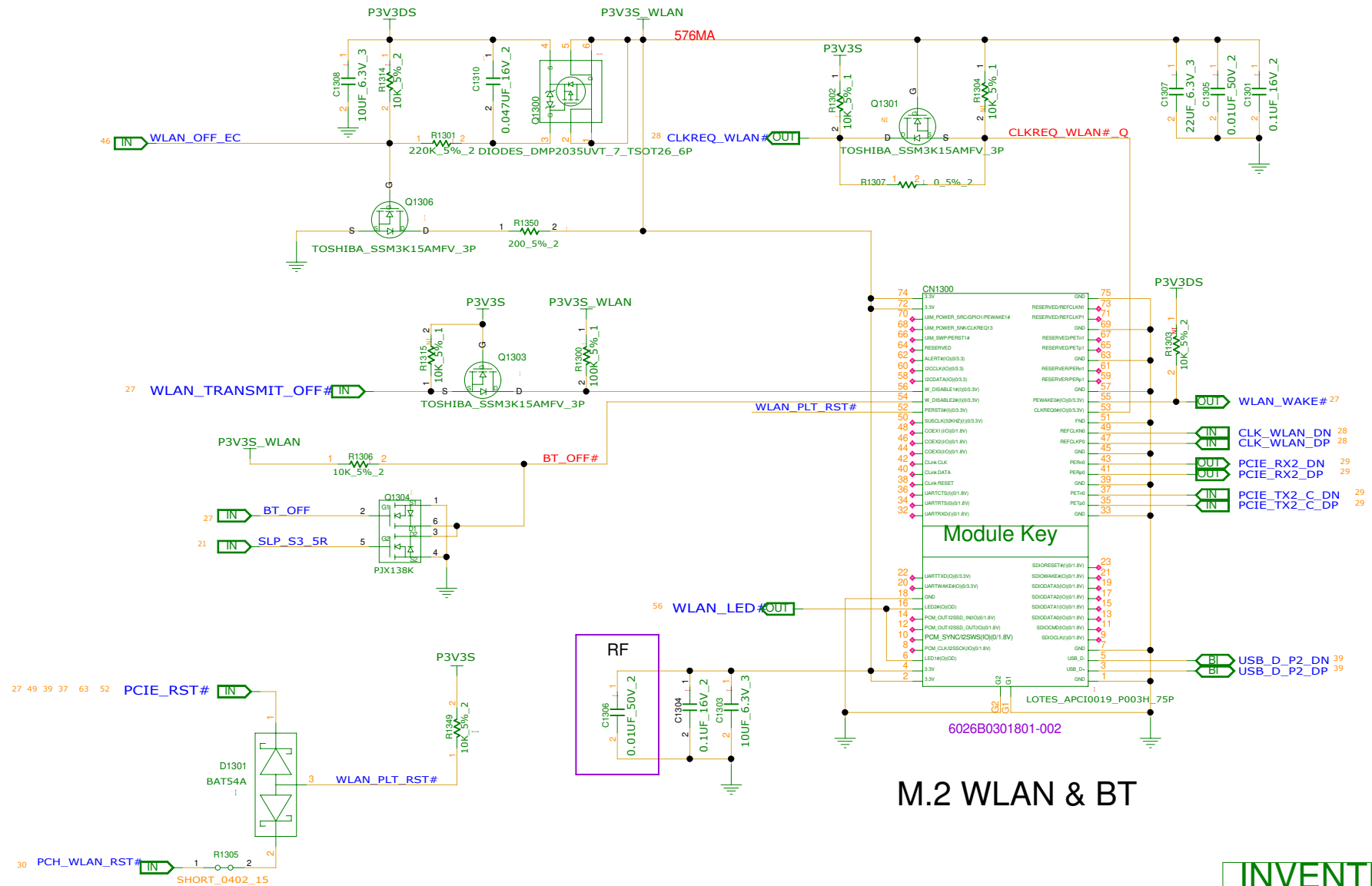
LAYOUT NOTE:
TO PLACE ONE 0.1UF AT EACH PIN 3, 4, 9, 10
AND PLACE THE 1UF IN THE SPOT THAT
IS AS CLOSE AS POSSIBLE TO ALL 4 PINS



INVENTEC

CHANGE by		XXX	DATE		21-OCT-2002
PCB P/N		60xxxxxxxxxxx	PCB VER		XXX

TITLE			
MODEL,PROJECT,FUNCTION TRANSFORMER & RJ45			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
SHEET		50 of 63	



M.2 WLAN & BT

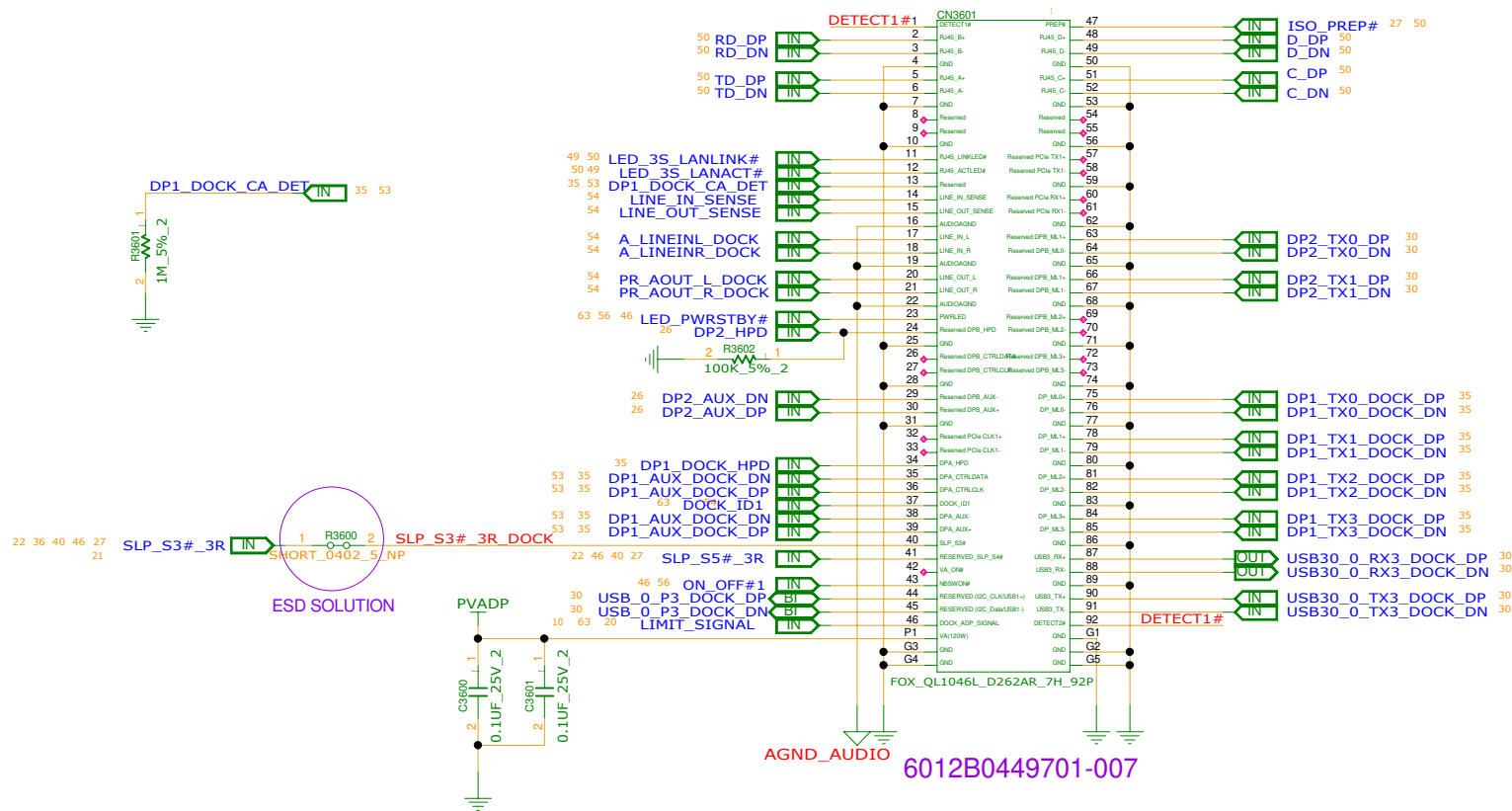
REFERENCE NUMBER:1300~1349

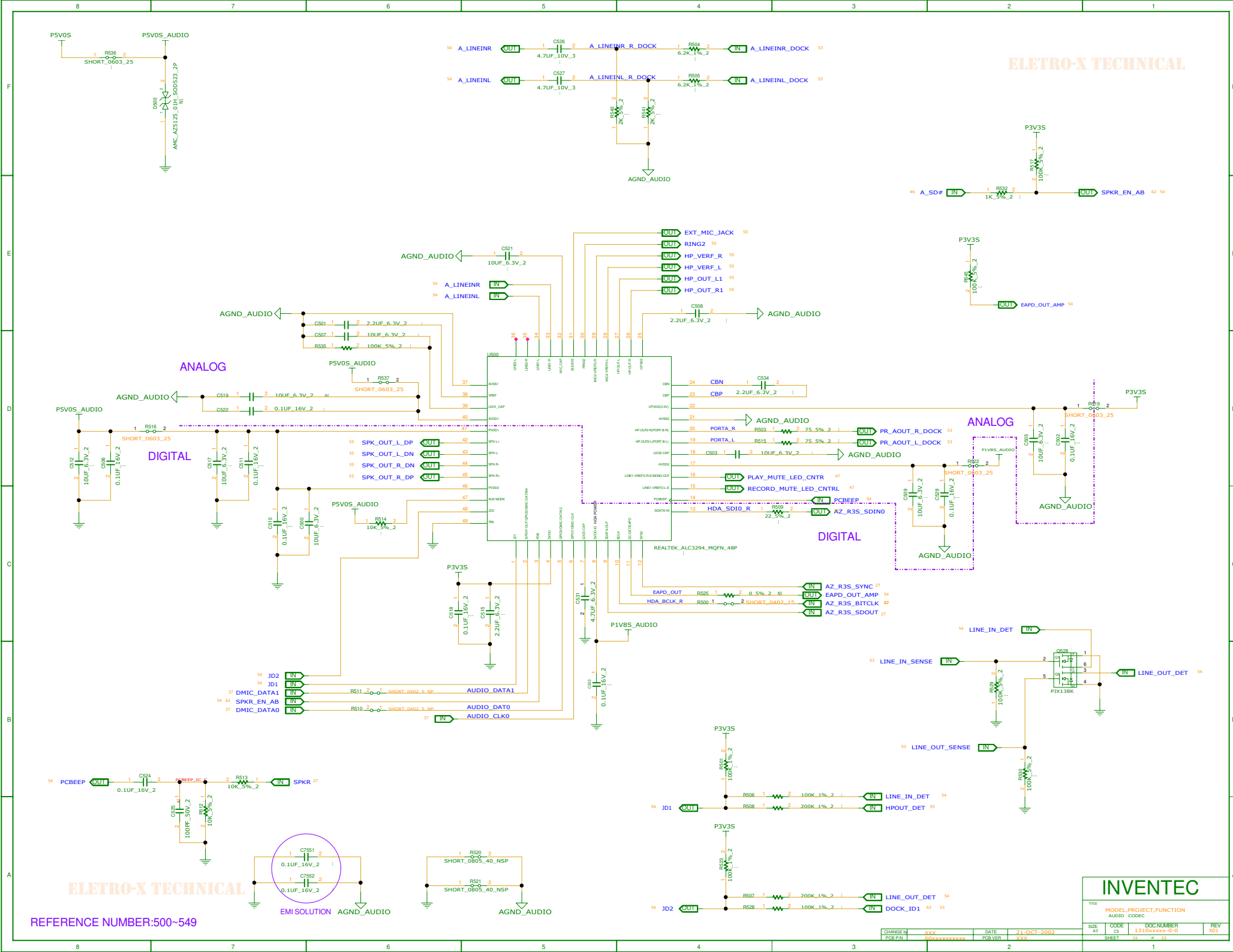
INVENTEC

TITLE	MODEL, PROJECT, FUNCTION
	Block Diagram

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
SHEET		51 of 63	

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX





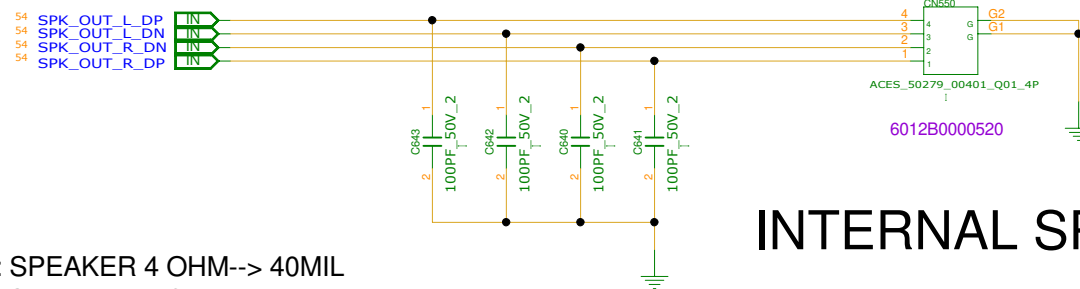
ELETR-X TECHNICAL

REFERENCE NUMBER:500~549

INVENTEC

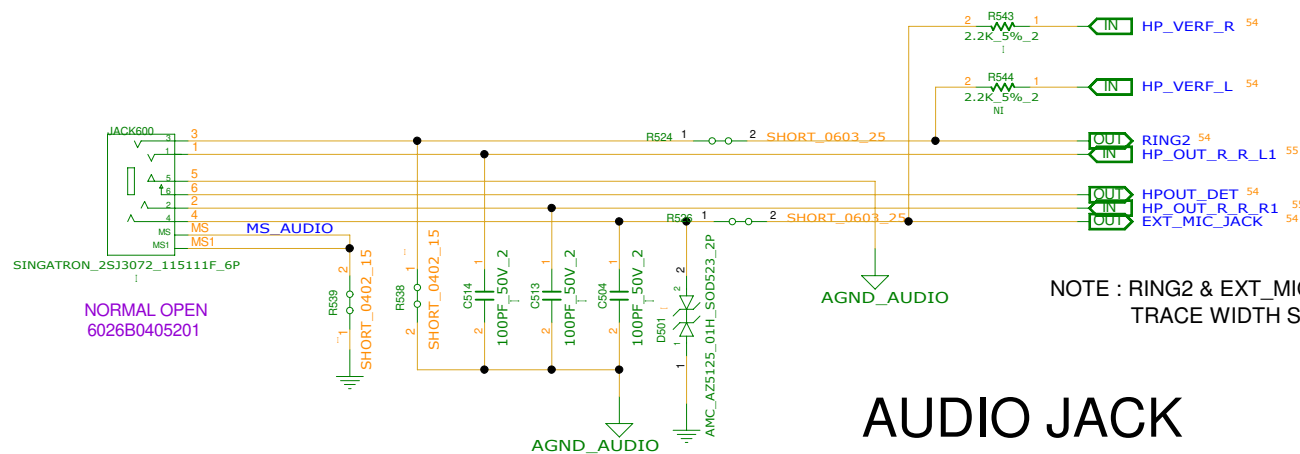
TITLE			
MODEL,PROJECT,FUNCTION			
AUDIO CODEC			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310XXXX-0-0	X01
SHEET	#	#	
52	#	53	

CHANGEN	XXX	DATE	21-OCT-2002
PCB PIN	PCB VER	PCB VER	XXX



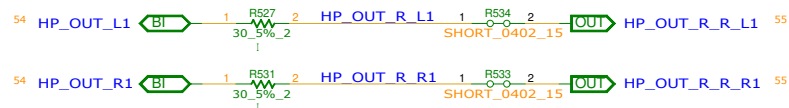
LAYOUT : SPEAKER 4 OHM--> 40MIL
SPEAKER 8 OHM--> 20MIL

INTERNAL SPEAKER



NOTE : RING2 & EXT_MIC_JACK
TRACE WIDTH SHOULD BE 40MIL LEAST

AUDIO JACK



REFERENCE NUMBER:600~649

INVENTEC

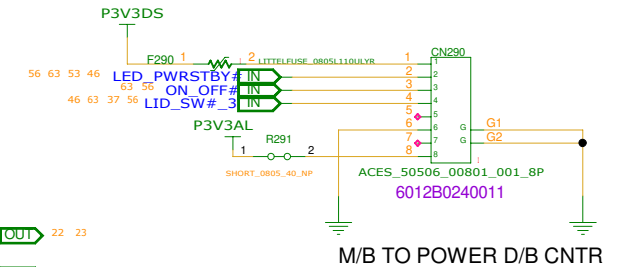
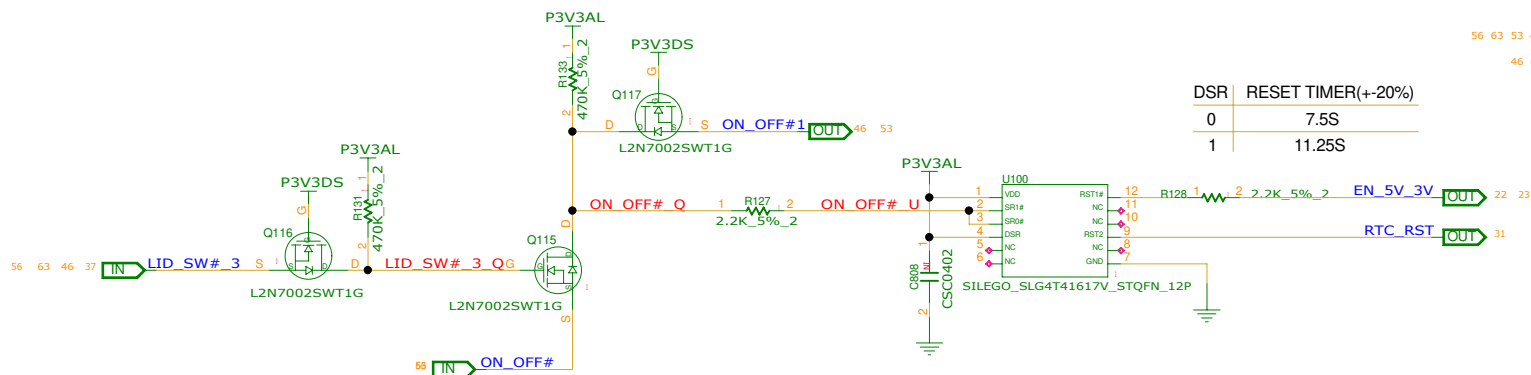
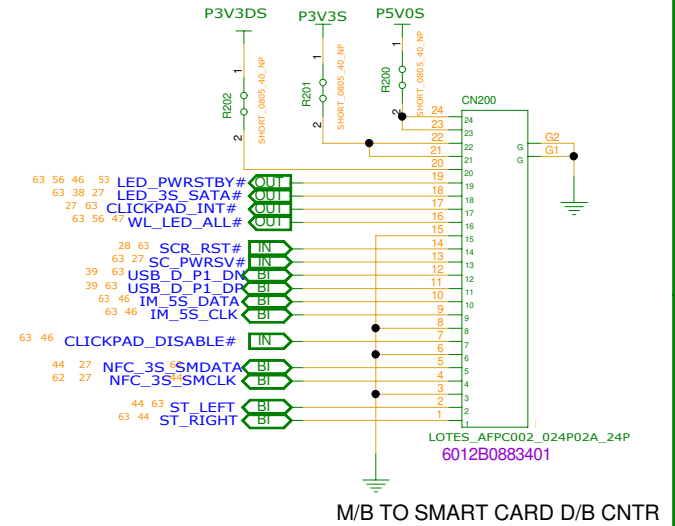
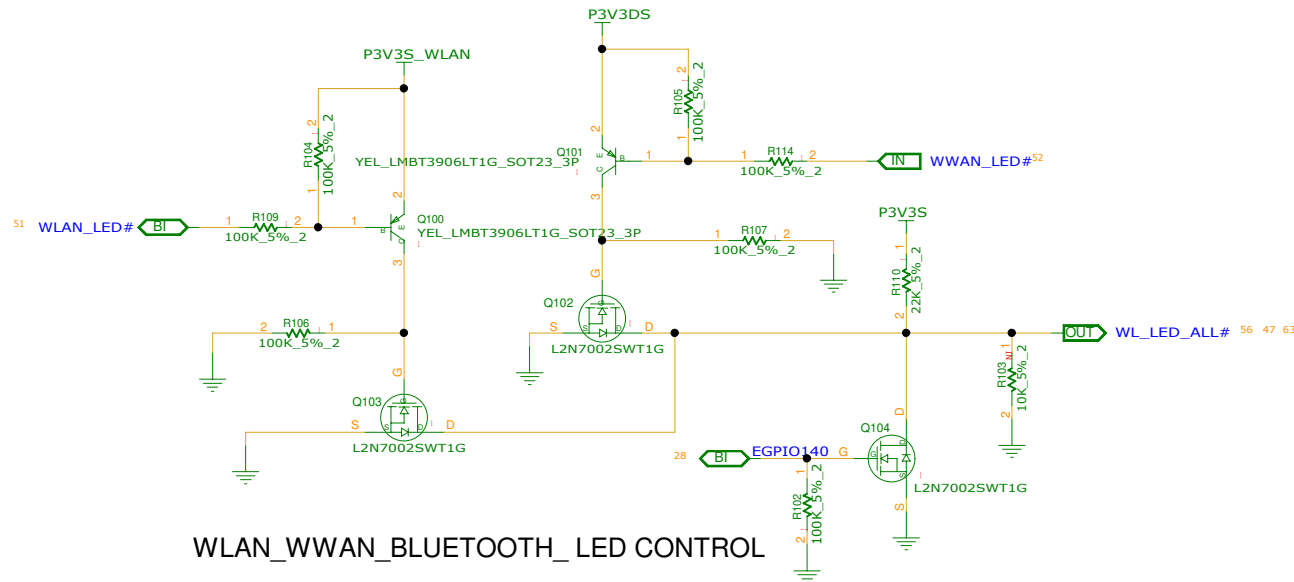
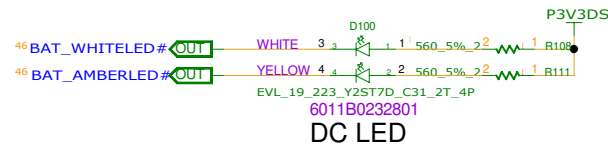
TITLE
MODEL PROJECT,FUNCTION
SPEAKER

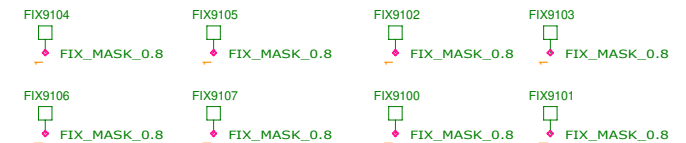
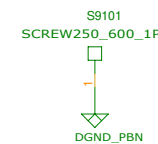
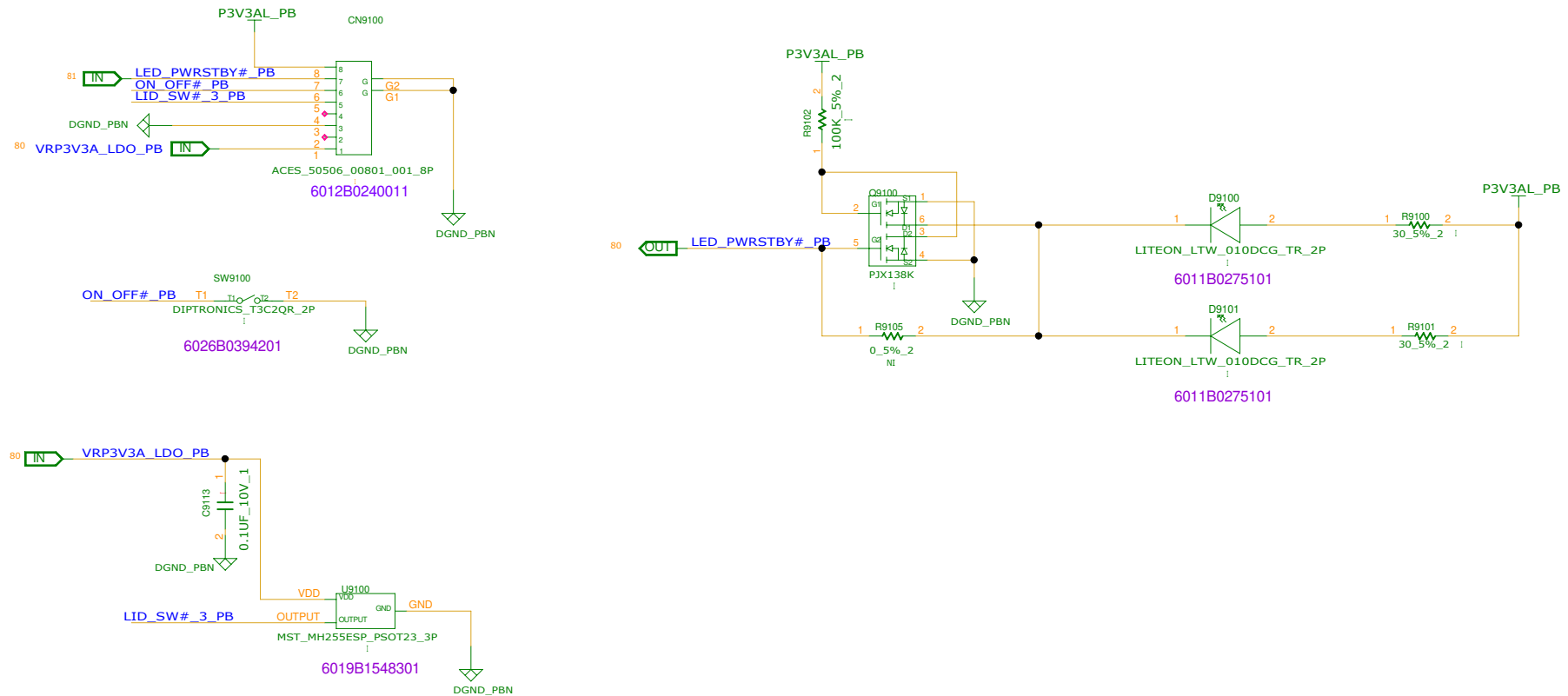
SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002
PCB P/N 60xxxxxxx PCB VER XXX

SHEET 55 of 63

REFERENCE NUMBER:100~199





POWER SWITCH DAUGHTER BOARD

REFERENCE NUMBER:9100~9199

ELETRO-X TECHNICAL

INVENTEC

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX

SIZE	A3	CODE	CS	DOC NUMBER	1310xxxxx-0-0	REV	X01
SHEET	57	of	63				

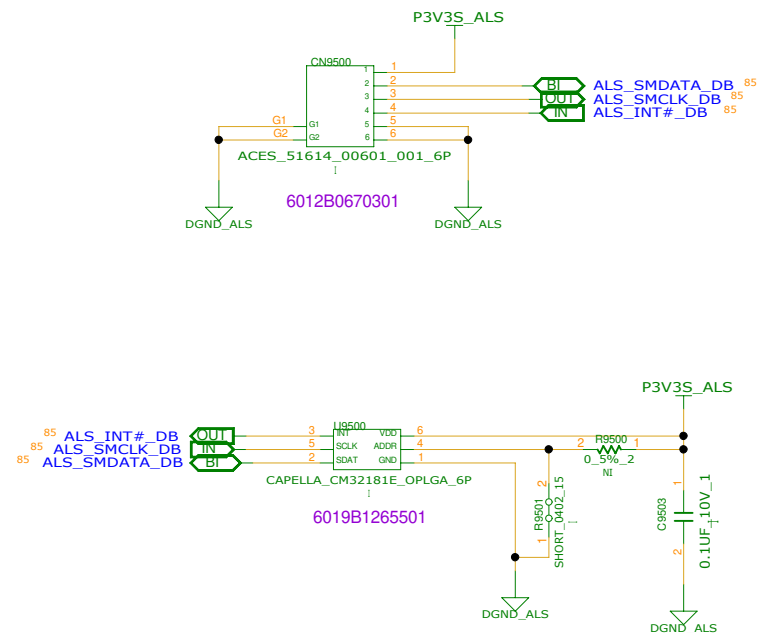


A

LOCATION NUMBER:9100~9199

INVENTEC			
TITLE MODEL,PROJECT,FUNCTION SMART CARD DB			
SIZE A3	CODE CS	DOC NUMBER 1310XXXXX-0-0	REV X01
SHEET 58 of 63			

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET		58	of 63



AMBIENT LIGHT SENSOR

ADDR IS HIGH, SELECT ADDRESS 0X48
ADDR IS LOW, SELECT ADDRESS 0X10

LOCATION NUMBER:9300~9349

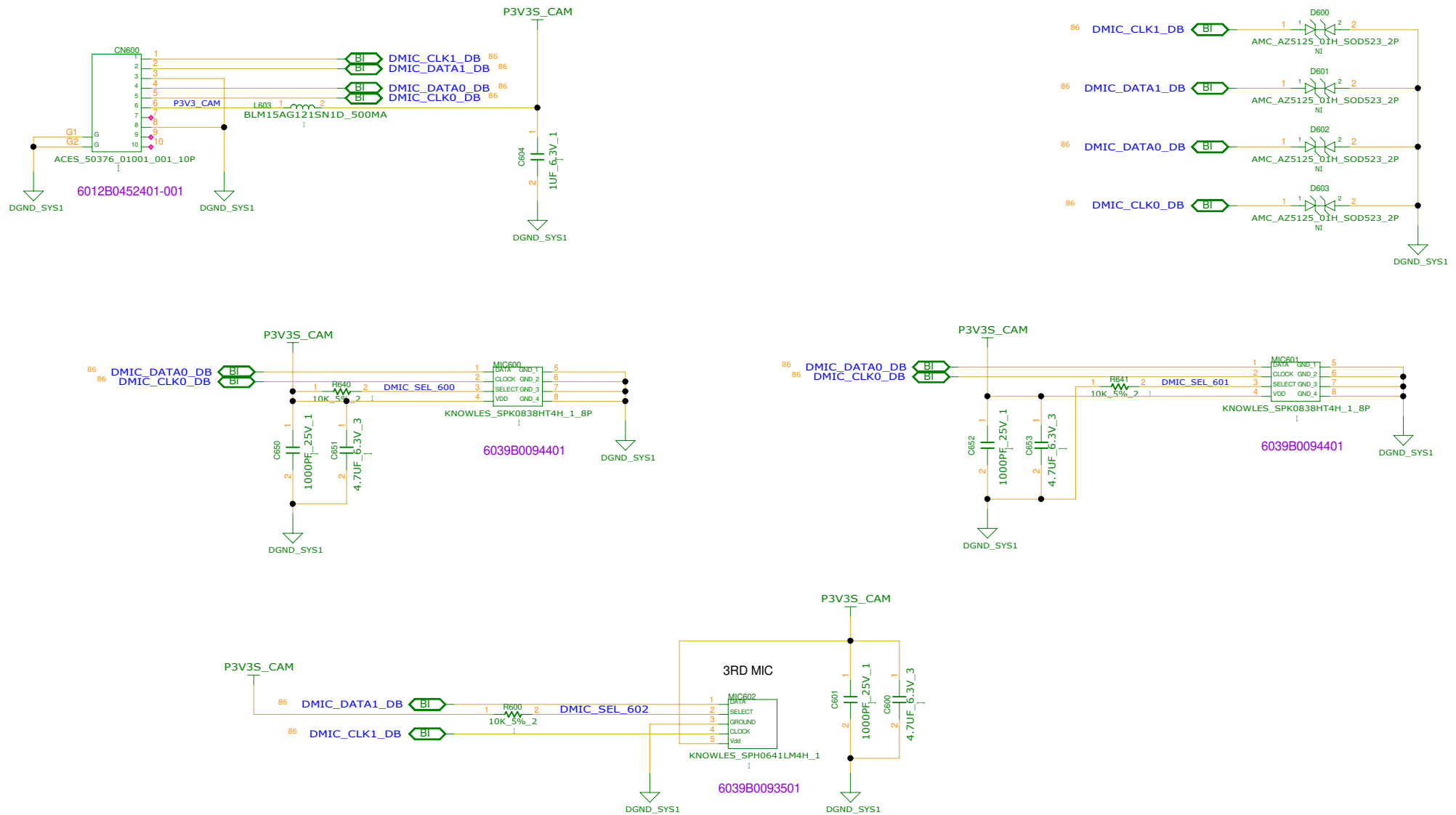
FIX3850
FIX_MASK_0.8

FIX3851
FIX_MASK_0.8

INVENTEC

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX

TITLE			
MODEL PROJECT,FUNCTION			
ALS D/B			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 59 of 63			



MIC DOUGHTER BOARD

LOCATION NUMBER: 9350~9399



FIX_MASK_0.8

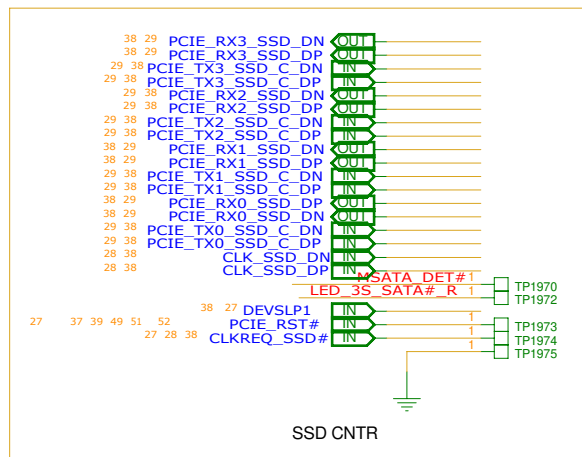
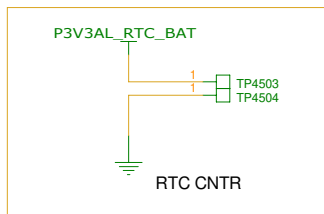
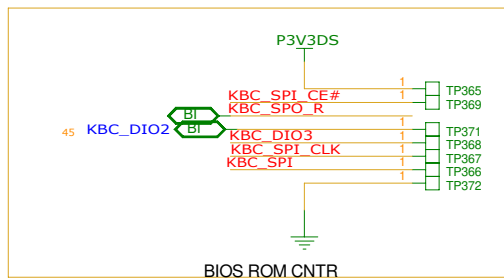
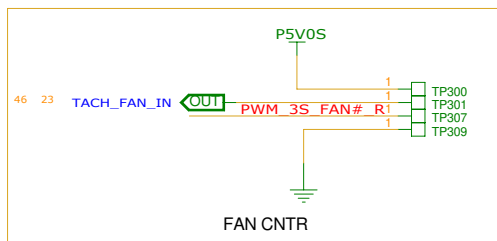
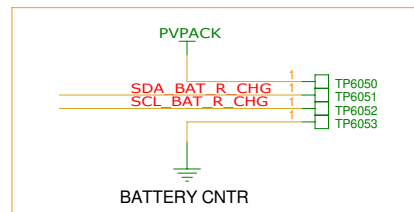
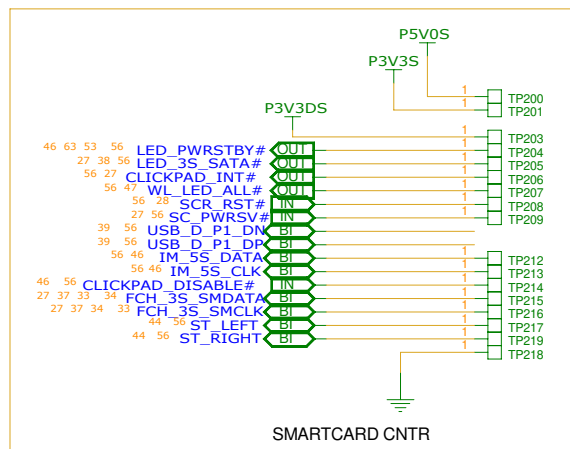
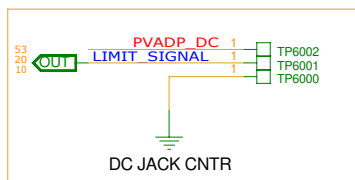
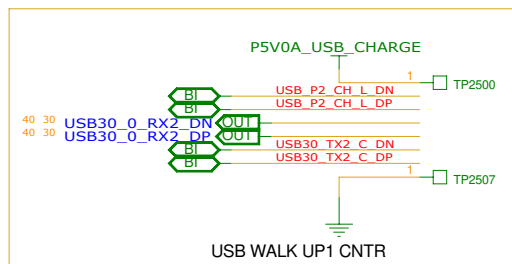
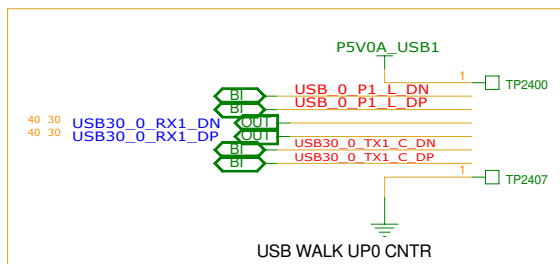
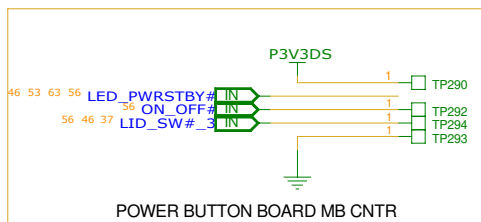


FIX_MASK_0.8

INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
MIC BOARD			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET	60 of 63		

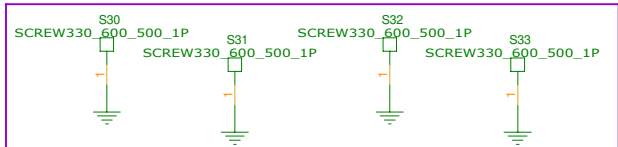
CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX



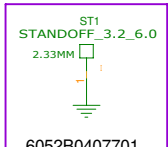
INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 63 of 63			

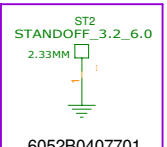
CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX



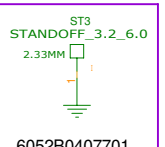
CPU SCREW



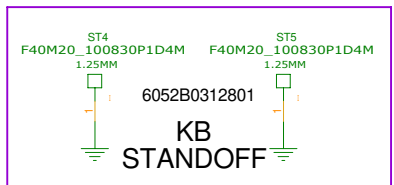
WLAN
STANDOFF



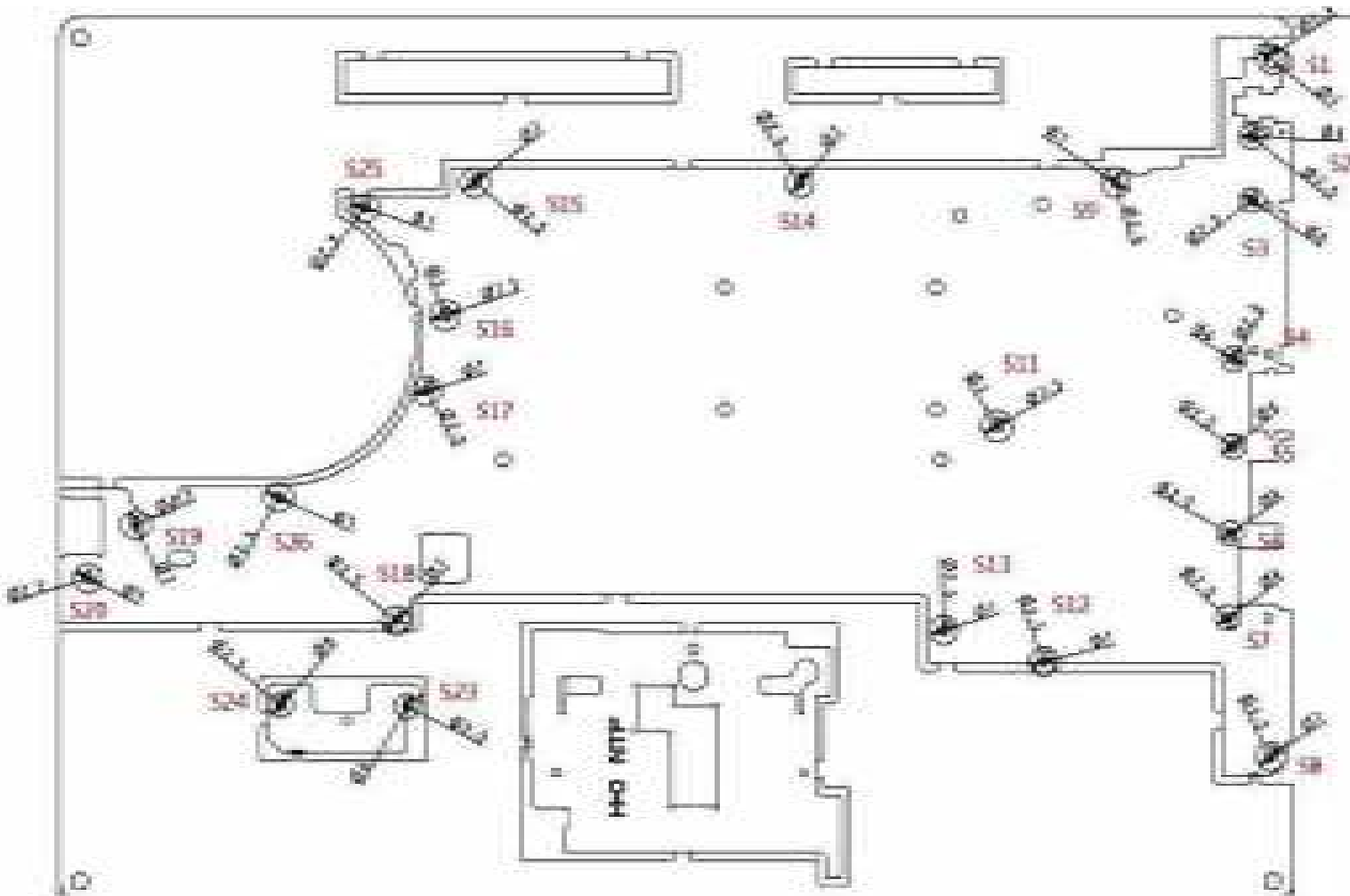
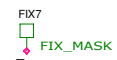
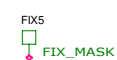
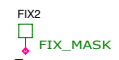
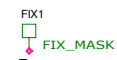
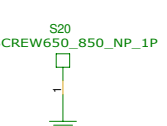
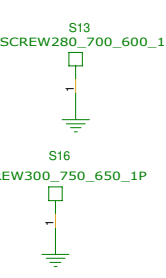
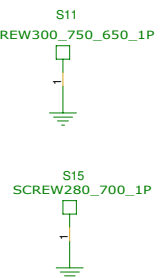
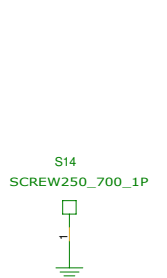
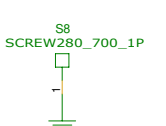
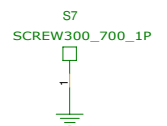
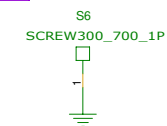
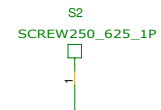
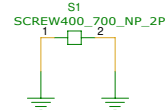
WWAN
STANDOFF



SSD
STANDOFF



KB
STANDOFF



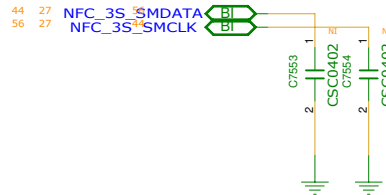
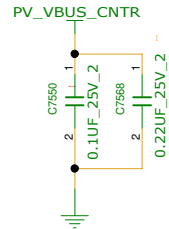
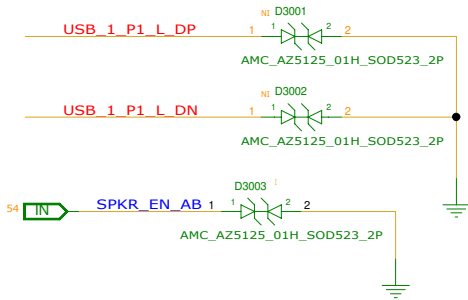
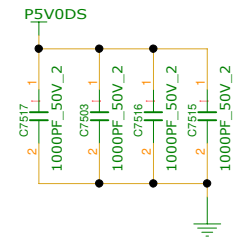
ELETRO-X TECHNICAL

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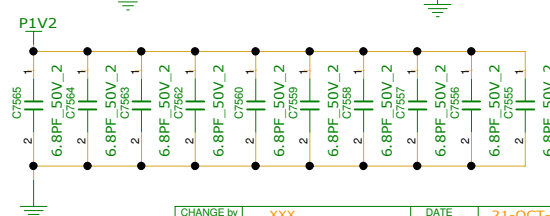
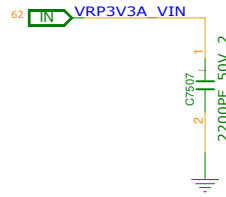
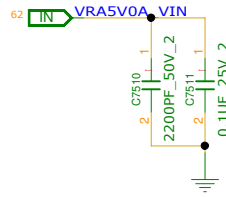
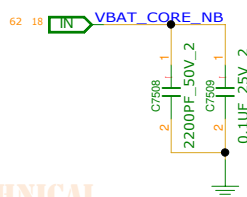
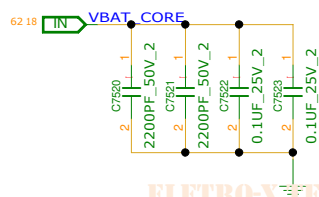
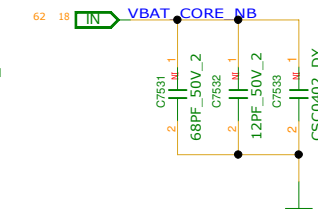
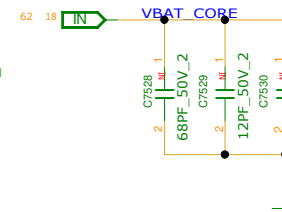
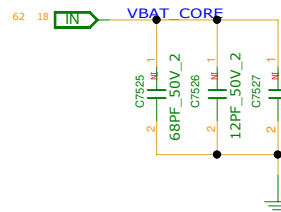
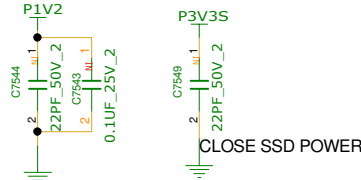
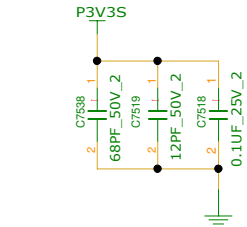
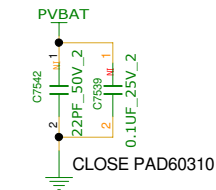
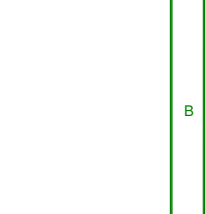
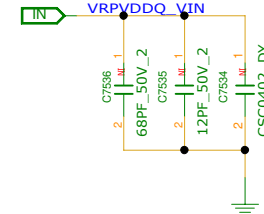
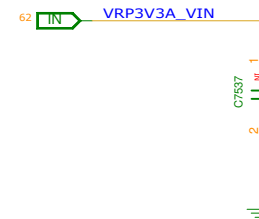
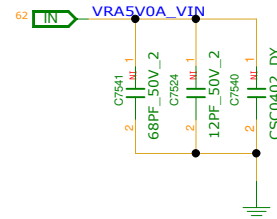
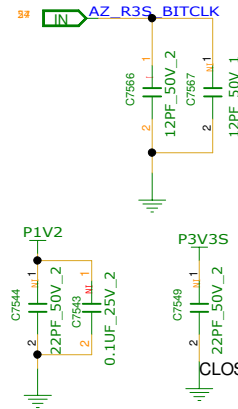
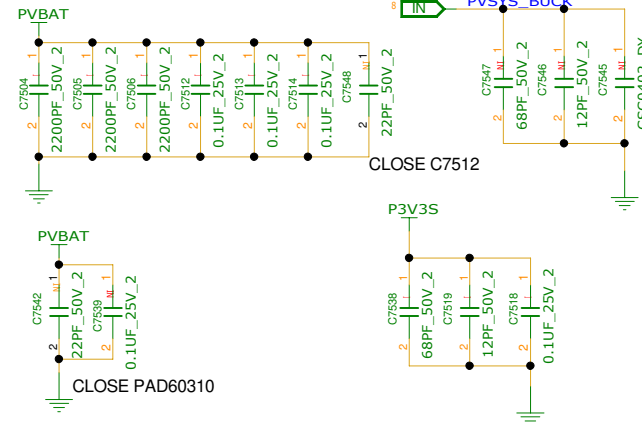
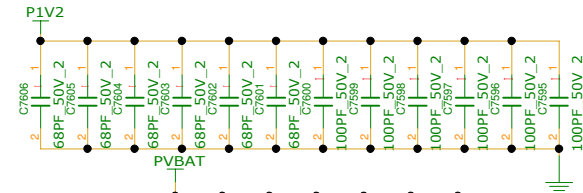
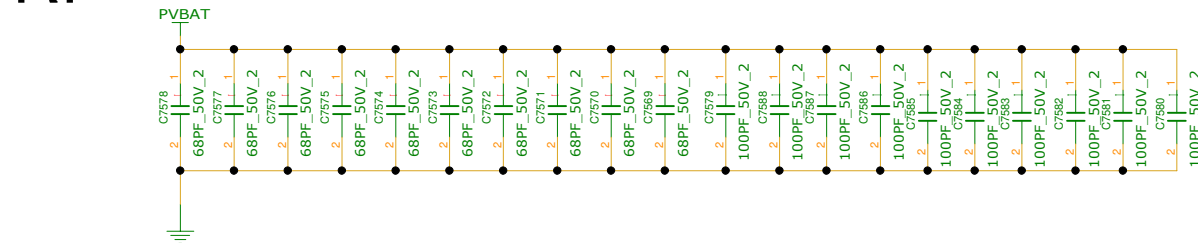
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EMI



ELETR-X TECHNICAL

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ELETR-X TECHNICAL

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CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxx	PCB VER	XXX

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